

ECENCAB12VU

Low-Capacitance Uni-directional Micro Packaged TVS Diodes for ESD Protection

The ECENCAB12VU is designed with ECORE TVS technology to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space comes at a premium.

It has been specifically designed to protect sensitive components which are connected to data and transmission lines from overvoltage caused by ESD(electrostatic discharge), and EFT (electrical fast transients).

Features

- Peak Power Dissipation –250 W (8 x 20 us Waveform)
- Stand-off Voltage: 12 V
- Low capacitance (<65.0pF) for high-speed interfaces
- Replacement for MLV (0402)
- Protects I/O Port
- Low Clamping Voltage
- Low Leakage
- Low Capacitance
- Response Time is < 1 ns
- Meets MSL 1 Requirements
- ROHS compliant
- Solid-state Punch-Through TVS Process technology

Main applications

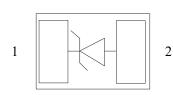
- Serial and Parallel Ports
- Notebooks, Desktops, Servers
- Projection TV
- Cellular handsets and accessories
- Portable instrumentation
- Peripherals
- MP3 Players

Protection solution to meet

- IEC61000-4-2 (ESD) ±30kV (air), ±30kV (contact)
- IEC61000-4-5 (Lightning) 10A (8/20μs)



DFN1006



Ordering Information

Device	Qty per Reel	Reel Size		
ECENCAB12VU	5000/10000	7 Inch		



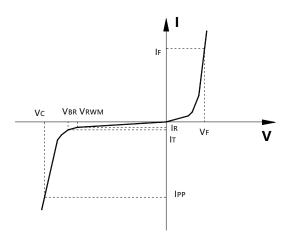
Maximum ratings (Tamb=25℃ Unless Otherwise Specified)						
Parameter	Symbol	Value	Unit			
Peak Pulse Power (tp=8/20μs waveform)	P _{PPP}	250	Watts			
ESD Rating per IEC61000-4-2: Contact		30	LV.			
Air		30	KV			
Lead Soldering Temperature	$T_{ m L}$	260 (10 sec.)	$^{\circ}$			
Operating Temperature Range	Tı	-55 ∼ 150	$^{\circ}$			
Storage Temperature Range	Tstg	-55 ∼ 150	$^{\circ}$			
Lead Solder Temperature – Maximum (10 Second Duration)	TL	260	$^{\circ}$			

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

Junction capacitance is measured in VR=0V, F=1MHz

Electric	Electrical characteristics (Tamb=25°C Unless Otherwise Specified)							
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units		
V _{RWM}	Reverse Working Voltage				12	V		
V _{BR}	Reverse Breakdown Voltage	$I_T = 1 \text{mA},$	13.3			V		
I_R	Reverse Leakage Current	$V_{RWM} = 12V$,		0.01	0.5	μΑ		
V_{F}	Forward voltage	$I_F = 10 \text{mA}$		0.9		V		
V _C	Clamping Voltage	$I_{PP} = 1A$, $tp = 8/20 \mu s$,		15	19	V		
		$I_{PP} = 10A$, $tp = 8/20 \mu s$,		17	25	V		
C_{J}	Junction Capacitance	$V_R = 0V$, $f = 1MHz$,			105	pF		

Symbol	Parameter		
Vrwm	Working Peak Reverse Voltage		
V _{BR}	Breakdown Voltage @ IT		
$V_{\rm C}$	Clamping Voltage @ IPP		
I_T	Test Current		
Irm	Leakage current at VRWM		
Ірр	Peak pulse current		
Co	Off-state Capacitance		
C _J	Junction Capacitance		

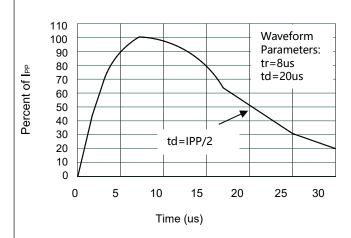


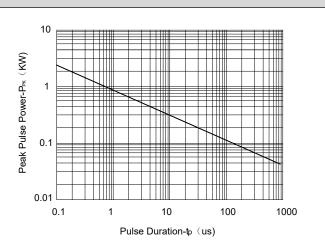
^{*}Other voltages may be available upon request.

^{1.} Non-repetitive current pulse, per Figure 1.



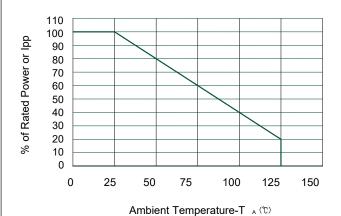






Pulse Waveform

Non-Repetitive Peak Pulse Power vs. Pulse Time

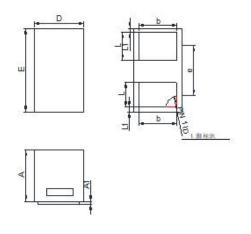


Power Derating Curve



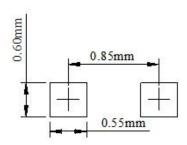
Package Information

DFN-1006

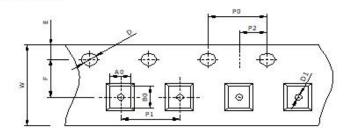


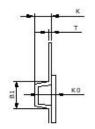
DIM	Millimeters		Inches		
DIM	Min	Max	Min	Max	
A	0.30	0.50	0.012	0.020	
Al	0.00	0.05	0.000	0.002	
D	0.55	0.65	0.022	0.026	
E	0.95	1.05	0.037	0.041	
b	0.25	0.60	0.010	0.024	
e	0.65TYP		0.026	TYP	
L	0.15	0.35	0.006	0.014	
Ll	0.05REF		0.002	REF	

Recommended Pad outline



DFN1006 Reel Dim





Package	Chip Size	Pocket Size B0×A0×K0(mm)	Tape Width	Reel Diameter	Quantity Per Reel	P0	Pl
DFN1006	1.0×0.6×0.50	1.10×0.70×0.60	8mm	178mm(7")	5000/10000	4mm	4/2mm
D0	D1	E	F	K	T	w	
1.5mm	0.5mm	1.75mm	3.5mm	0.55mm	0.2mm	8mm	