

N-Channel 40V(D-S) MOSFET

Product summary		
V_{DS}	40	V
$R_{DS(ON)}$ (at $V_{GS}=10V$) Typ.	6.4	$m\Omega$
$R_{DS(ON)}$ (at $V_{GS}=4.5V$) Typ.	9.4	$m\Omega$
$I_D(T_c=25^\circ C)$	19	A

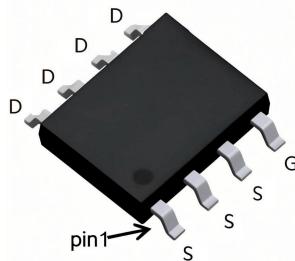
Features

- High density cell design for low $R_{DS(ON)}$
- Single Drive Requirement
- Fast Switching Characteristic

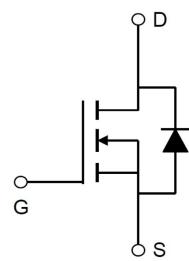
Applications

- Power management functions

Pin Configuration



SOP8



Packing Information

Device	Marking	Reel Size	Tape Width	Quantity
ECHA19N04S	D07N04	13"	12mm	2500pcs

Absolute Maximum Ratings (at $TA=25^\circ C$ Unless Otherwise Noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current at $V_{GS}=10V$	19	A
		13	A
I_{DM}	Pulse Drain Current Tested ^A	64	A
E_{AS}	Single Pulse Avalanche Energy	256	mJ
P_D	Power Dissipation	3.1	W
T_J, T_{STG}	Junction and Storage Temperature Range	-55 to +150	°C

Thermal Characteristics

Symbol	Parameter	Typical	Units
R_{eJA}	Thermal Resistance-Junction to ambient ^B	40	°C/W

Electrical Characteristics (at $T_J = 25^\circ\text{C}$ Unless Otherwise Noted)

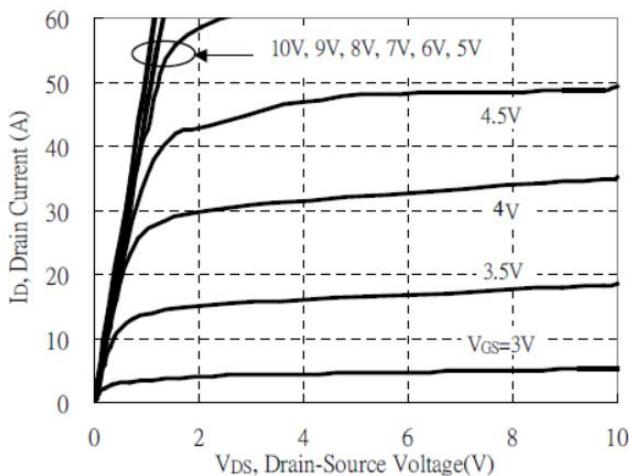
Symbol	Parameter	Condition	Min.	Typ.	Max.	Units
Static Parameters						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	40	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=32\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 20\text{V}$	--	--	± 100	nA
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.5	--	3.0	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=11\text{A}$	--	6.4	8	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=8\text{A}$	--	9.4	12	$\text{m}\Omega$
V_{SD}	Forward Voltage	$I_{\text{S}}=11\text{A}, V_{\text{GS}}=0\text{V}$	--	--	1.2	V
I_{SM}	Maximum Body-Diode Continuous Current		--	--	19	A
Dynamic Parameters						
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=25\text{V}$ $f=1\text{MHz}$	--	1421	--	pF
C_{oss}	Output Capacitance		--	172	--	pF
C_{rss}	Reverse Transfer Capacitance		--	126	--	pF
Switching Parameters						
Q_g	Total Gate Charge	$V_{\text{DS}}=32\text{V}, I_{\text{D}}=16\text{A}$ $V_{\text{GS}}=10\text{V}$	--	32.3	--	nC
Q_{gs}	Gate-Source Charge		--	5.5	--	nC
Q_{gd}	Gate-Drain Charge		--	9.9	--	nC
$t_{\text{D}(\text{on})}$	Turn-on Delay Time	$V_{\text{DD}}=20\text{V}$ $I_{\text{D}}=1\text{A}, R_{\text{G}}=1\Omega$ $V_{\text{GS}}=10\text{V}$	--	14.6	--	nS
t_r	Turn-on Rise Time		--	18.8	--	nS
$t_{\text{D}(\text{off})}$	Turn-off Delay Time		--	43.4	--	nS
t_f	Turn-off Fall Time		--	8	--	nS

A. Pulse width limited by maximum junction temperature

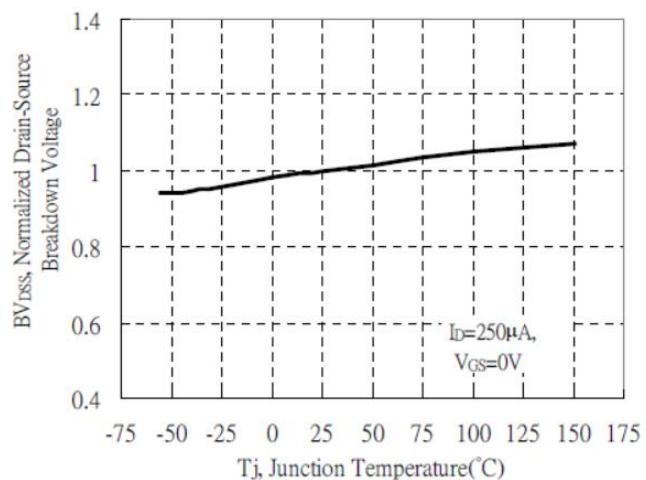
B. $40^\circ\text{C}/\text{W}$ when mounted on a 1 in2 pad of 2 oz copper, $t \leq 10\text{s}$; $125^\circ\text{C}/\text{W}$ when mounted on minimum pad.

Typical Characteristics

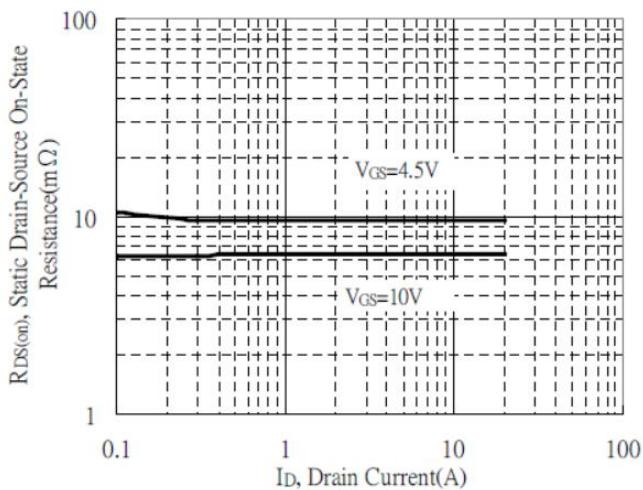
Typical Output Characteristics



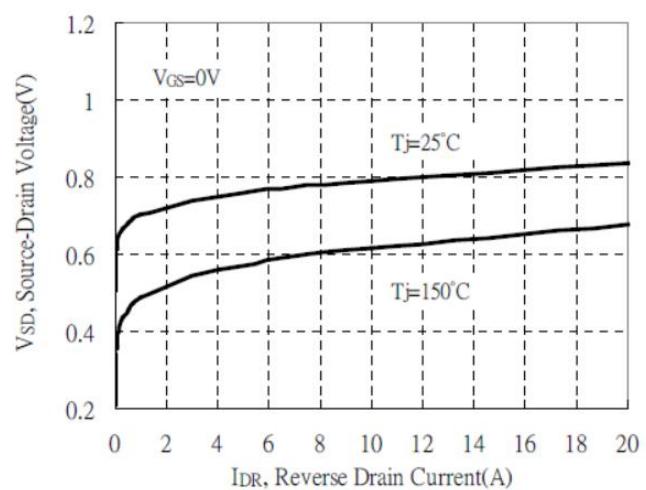
Breakdown Voltage vs Ambient Temperature



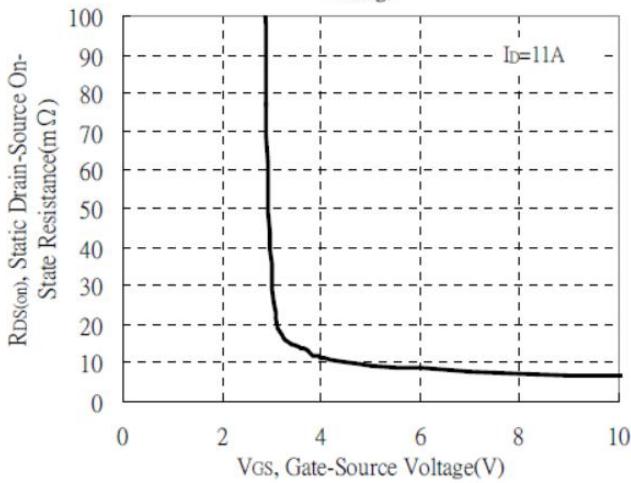
Static Drain-Source On-State resistance vs Drain Current



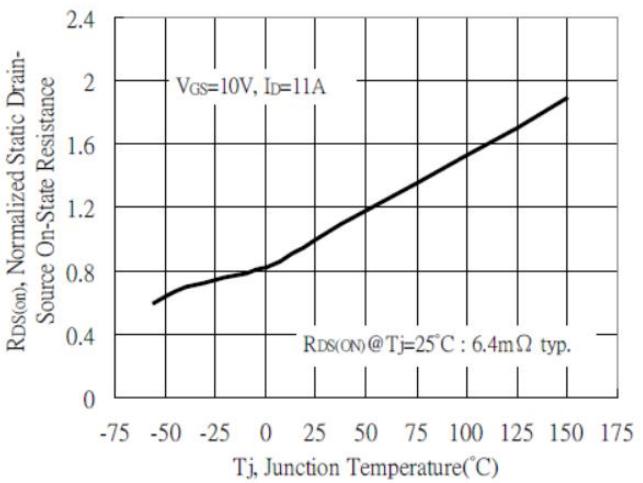
Reverse Drain Current vs Source-Drain Voltage



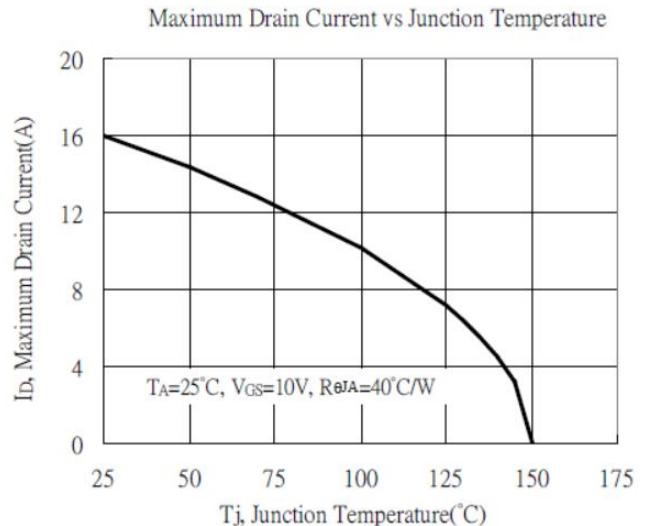
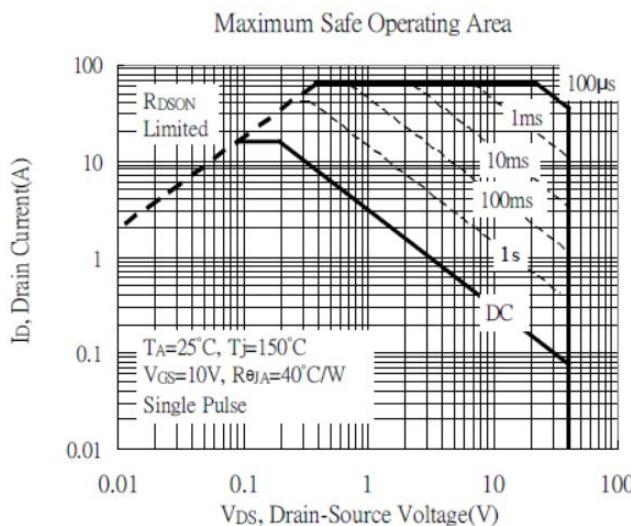
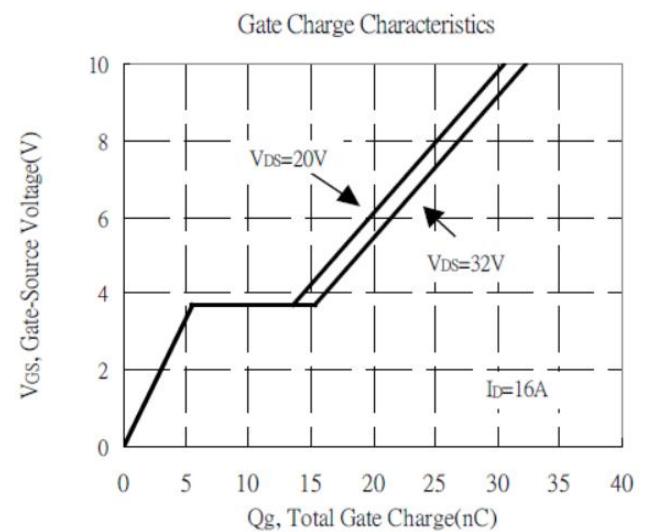
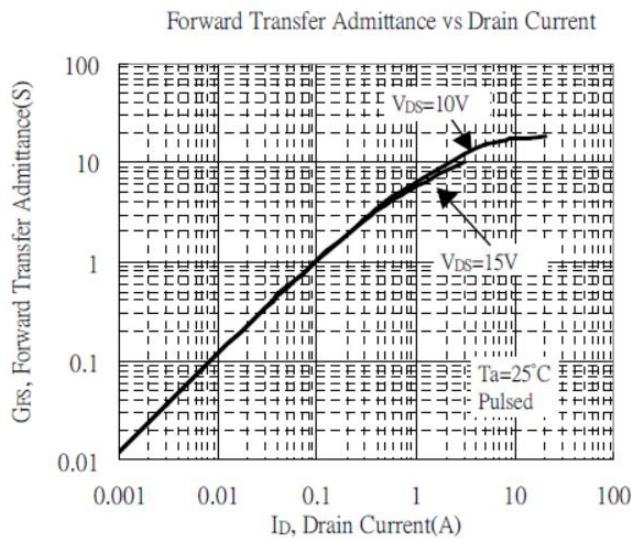
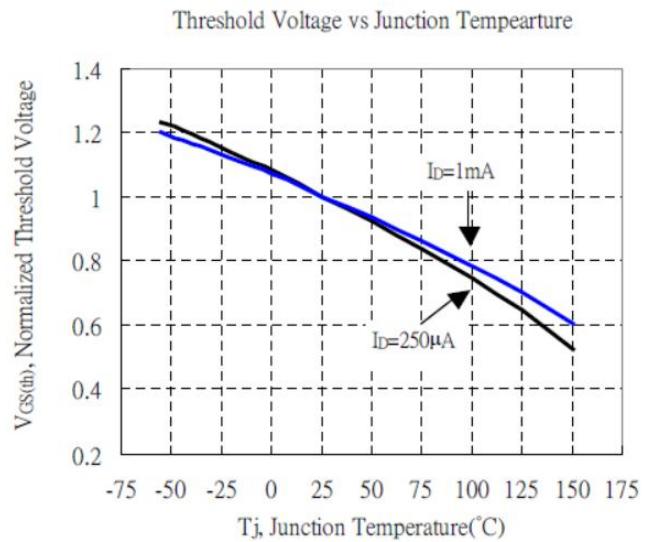
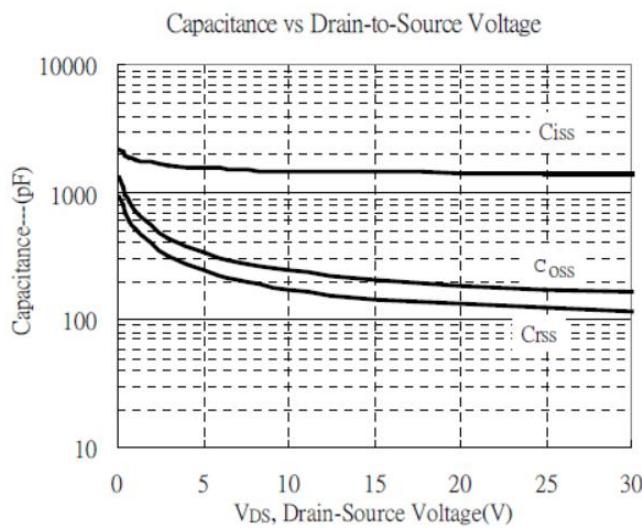
Static Drain-Source On-State Resistance vs Gate-Source Voltage



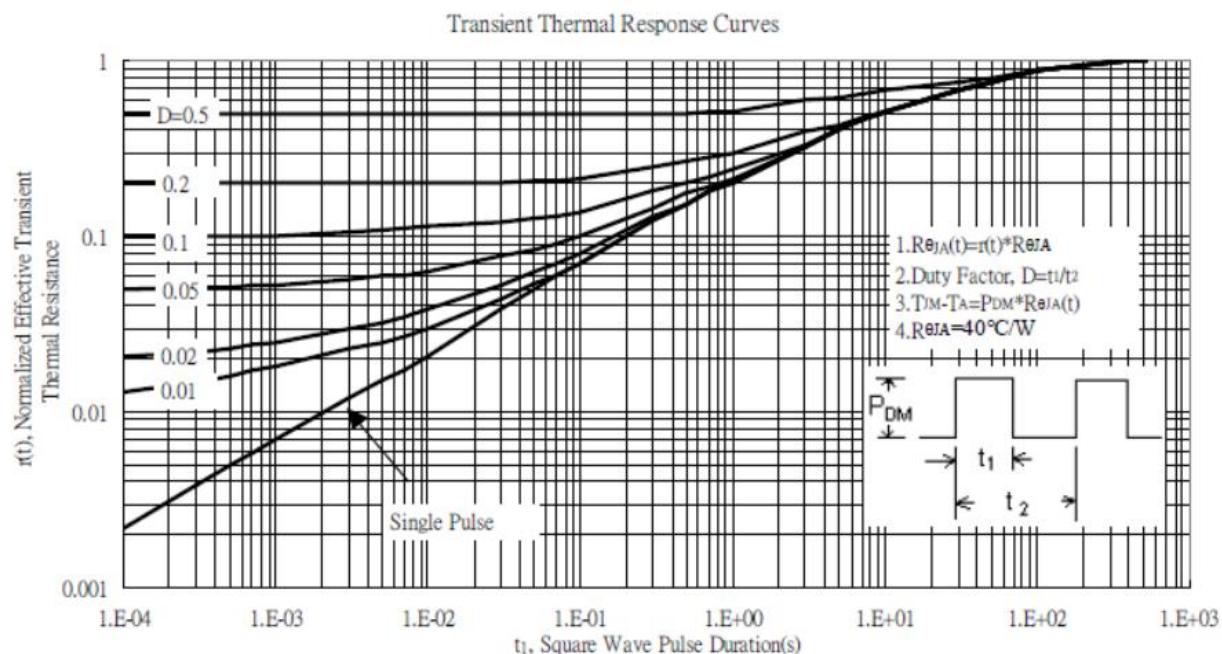
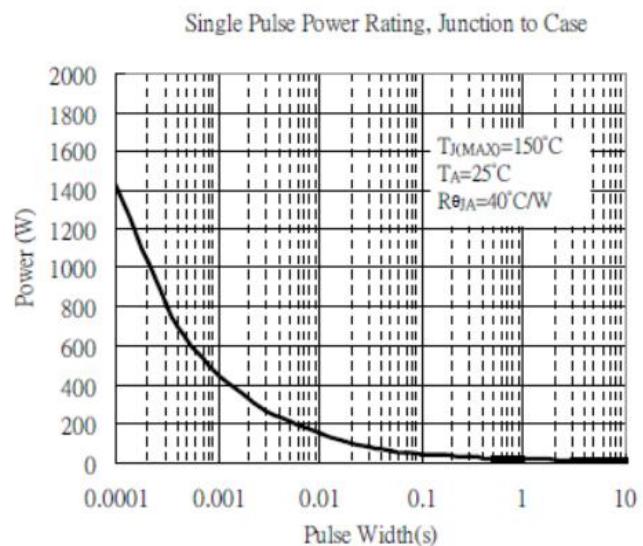
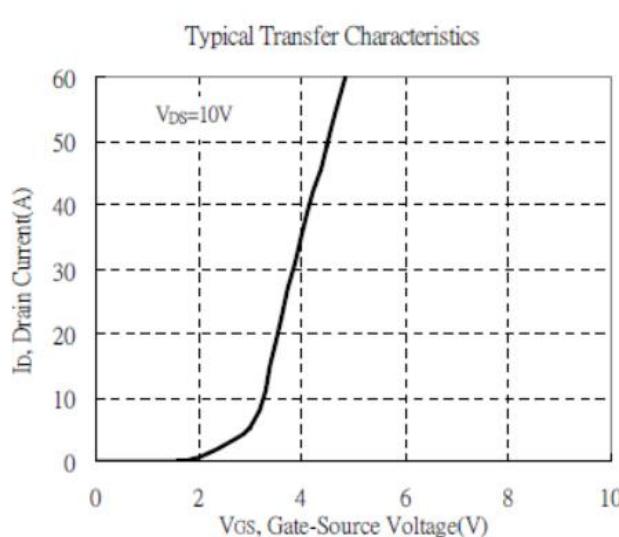
Drain-Source On-State Resistance vs Junction Temperature



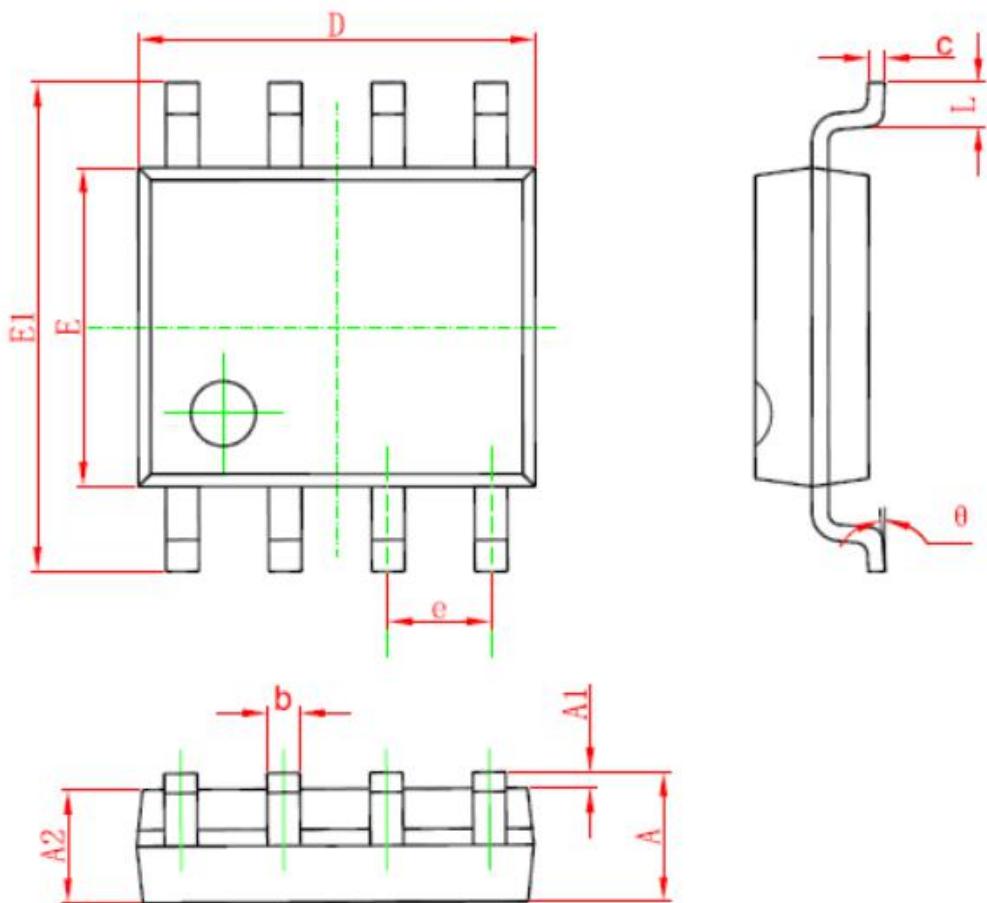
Typical Characteristics



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SOP8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°