

## P-Channel 20V(D-S) MOSFET

Product summary		
$V_{DS}$	-20	V
$R_{DS(ON)}$ (at $V_{GS}=-4.5V$ ) Typ.	17	m $\Omega$
$R_{DS(ON)}$ (at $V_{GS}=-2.5V$ ) Typ.	20	m $\Omega$
$I_D$ ( $T_A=25^\circ C$ )	-6.3	A

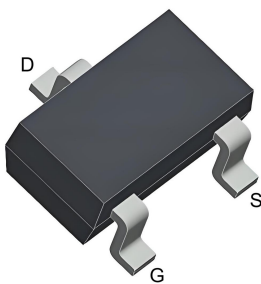
### Features

- Trench LV MOSFET Technology
- Low  $R_{DS(ON)}$

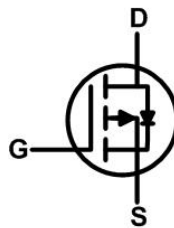
### Applications

- Power management
- Load Switch

### Pin Configuration



SOT23-3



### Packing Information

Device	Package	Reel Size	Quantity(Min. Package)
EC2107	SOT23-3	7"	3000pcs

### Absolute Maximum Ratings (at $T_A=25^\circ C$ Unless Otherwise Noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-20	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	V
$I_D$	Continuous Drain Current	$T_A=25^\circ C$	-6.3
		$T_A=70^\circ C$	-5.0
$I_{DM}$	Pulse Drain Current Tested <sup>A</sup>	-22	A
$P_D$	Power Dissipation <sup>B</sup>	1.4	W
$T_J, T_{STG}$	Junction and Storage Temperature Range	-55 to +150	$^\circ C$

### Thermal Characteristics

Symbol	Parameter	Typical	Units
$R_{\theta JA}$	Thermal Resistance-Junction to ambient <sup>B</sup>	89	$^\circ C/W$

**Electrical Characteristics (at  $T_J = 25^\circ\text{C}$  Unless Otherwise Noted)**

Symbol	Parameter	Condition	Min.	Typ.	Max.	Units
<b>Static Parameters</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-20	--	--	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=-20V, V_{GS}=0V$	--	--	-1	$\mu A$
$I_{GSS}$	Gate-Body Leakage Current	$V_{DS}=0V, V_{GS}=\pm 12V$	--	--	$\pm 100$	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.4	-0.65	-1.0	V
$R_{DS(ON)}$	Drain-Source On-State Resistance <sup>C</sup>	$V_{GS}=-4.5V, I_D=-4A$	--	17	23	m $\Omega$
		$V_{GS}=-2.5V, I_D=-3A$	--	20	29	m $\Omega$
$V_{SD}$	Drain to Source Diode Forward Voltage	$I_{SD}=-6.3A, V_{GS}=0V$	--	--	-1.2	V
<b>Dynamic Parameters <sup>D</sup></b>						
$C_{iss}$	Input Capacitance	$V_{GS}=0V, V_{DS}=-10V$ $f=1\text{MHZ}$	--	1450	--	pF
$C_{oss}$	Output Capacitance		--	220	--	pF
$C_{riss}$	Reverse Transfer Capacitance		--	203	--	pF
$Q_g$	Total Gate Charge	$V_{DS}=-10V, I_D=-3A$ $V_{GS}=-4.5V$	--	14.4	--	nC
$Q_{gs}$	Gate-Source Charge		--	2.1	--	nC
$Q_{gd}$	Gate-Drain Charge		--	3.5	--	nC
$t_{D(on)}$	Turn-on Delay Time	$V_{DD}=-10V,$ $I_D=-3A, V_{GS}=-4.5V,$ $R_G=3\Omega$	--	19	--	ns
$t_r$	Turn-on Rise Time		--	26	--	ns
$t_{D(off)}$	Turn-off Delay Time		--	24	--	ns
$t_f$	Turn-off Fall Time		--	6.3	--	ns

A. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

B. The data tested by surface mounted on a 1 inch x 1 inch FR-4 board with 20Z copper.

C. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$ .

D. Guaranteed by design, not subject to production testing.

Typical Characteristics

Figure 1. Output Characteristics

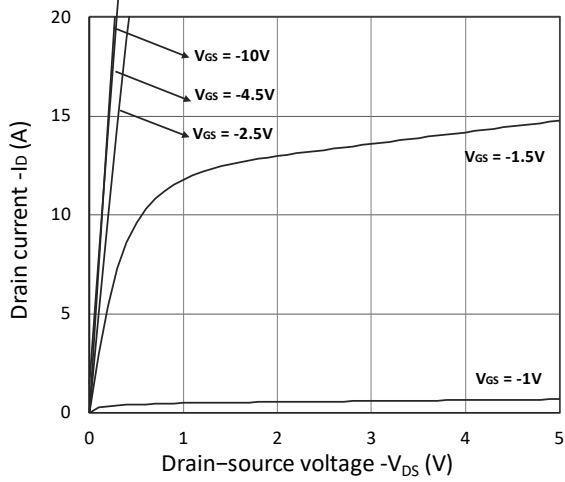


Figure 2. Transfer Characteristics

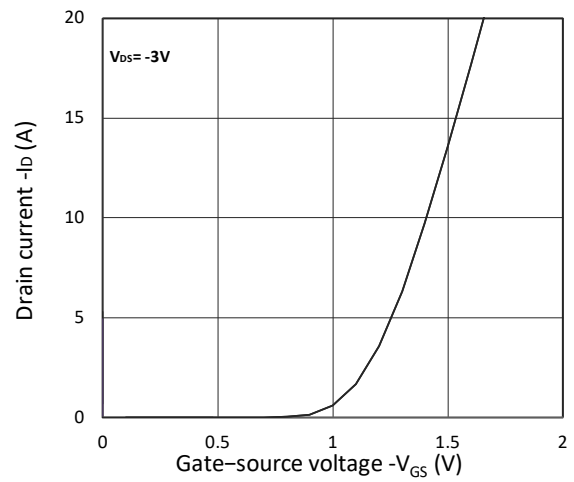


Figure 3. Forward Characteristics of Reverse

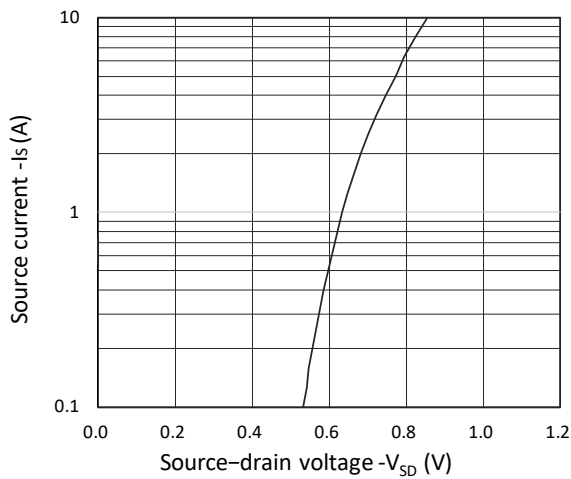


Figure 4.  $R_{DS(ON)}$  vs.  $V_{GS}$

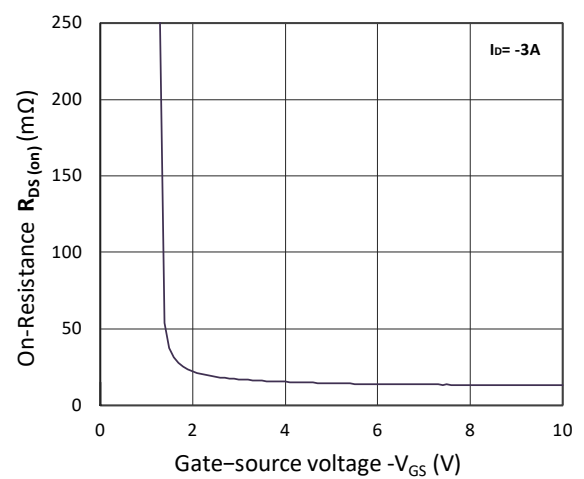


Figure 5.  $R_{DS(ON)}$  vs.  $I_D$

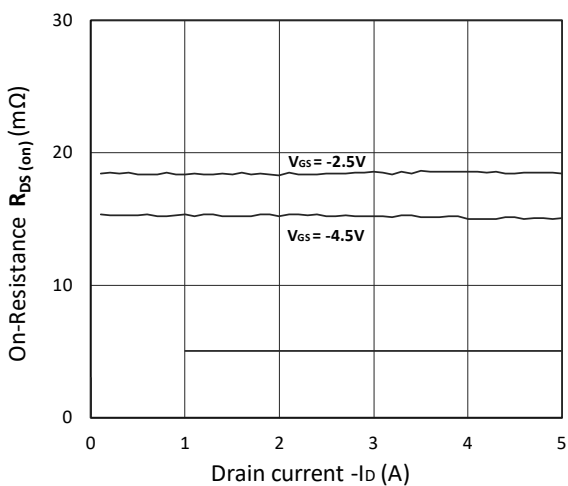
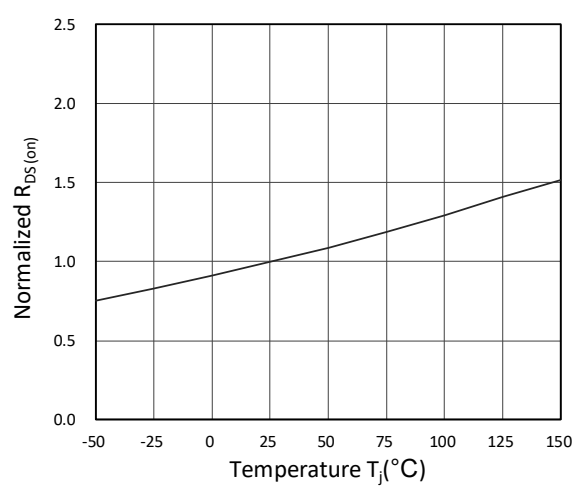


Figure 6. Normalized  $R_{DS(on)}$  vs. Temperature



Typical Characteristics

Figure 7. Capacitance Characteristics

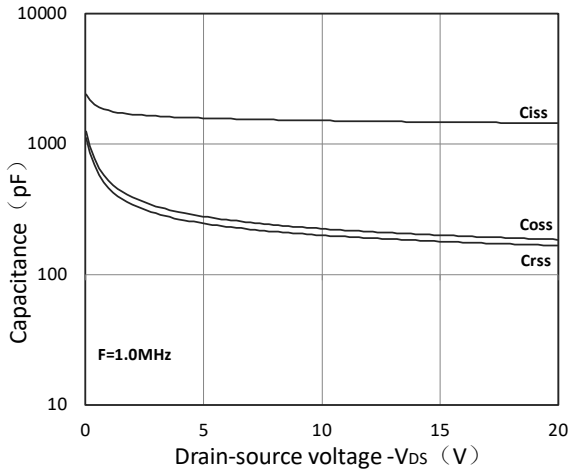
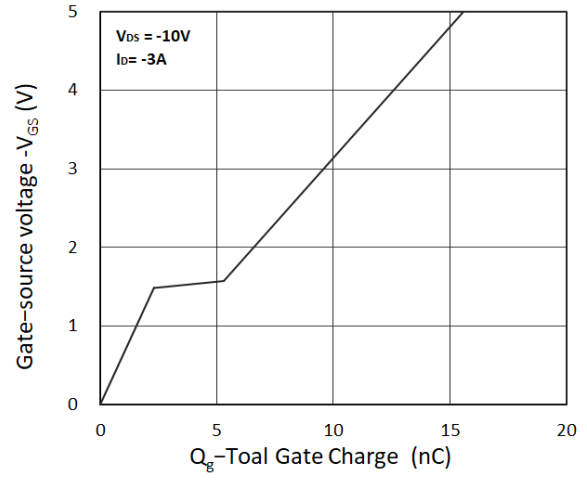
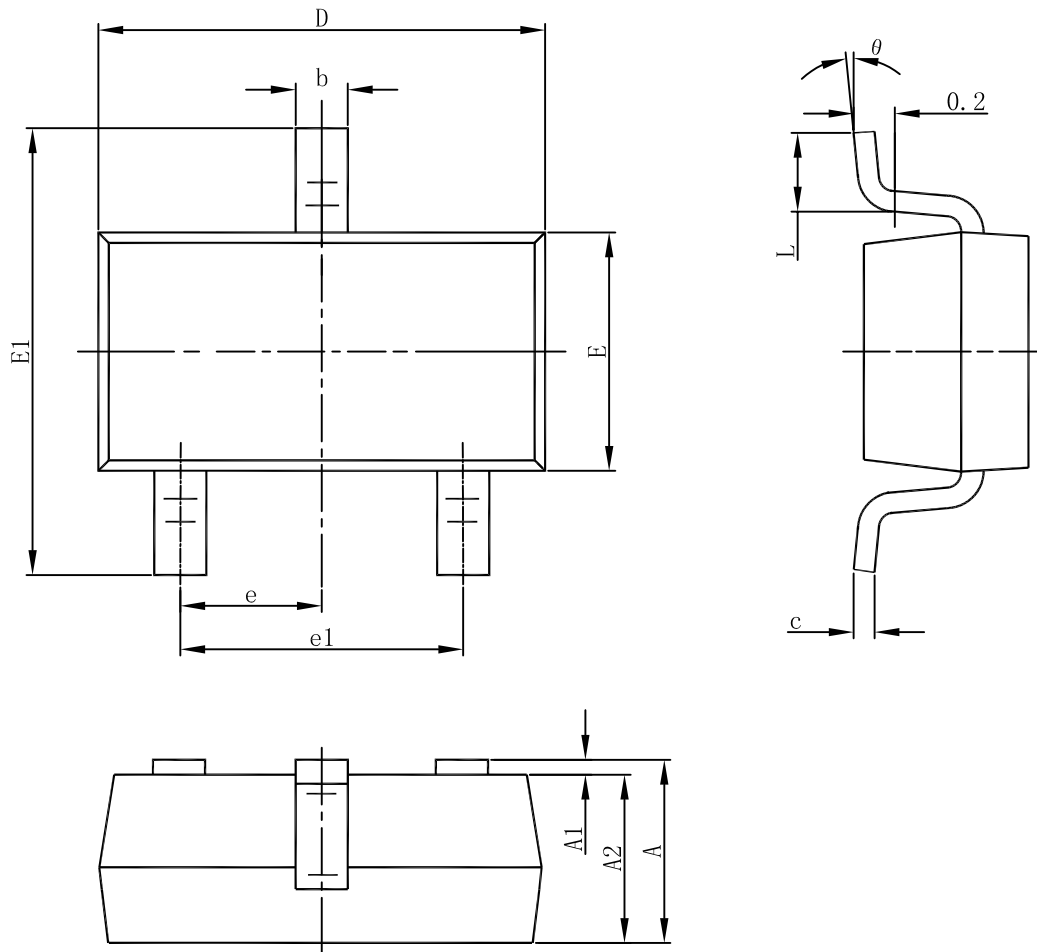


Figure 8. Gate Charge Characteristics



## SOT23-3 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
$\theta$	0°	8°	0°	8°