

N-Channel and P-Channel 20V(D-S) MOSFET

| Product summary | | | |
|--|------|-------|-----------|
| V_{DS} | 20 | -20 | V |
| $R_{DS(ON)}$ (at $V_{GS}=4.5V$) Typ. | 250 | 350 | $m\Omega$ |
| $R_{DS(ON)}$ (at $V_{GS}=2.5V$) Typ. | 300 | 440 | $m\Omega$ |
| $I_D(T_A=25^\circ C)$ | 0.65 | -0.55 | A |

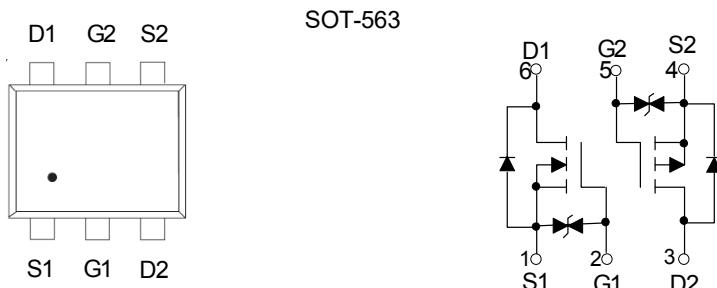
Features

- Low $R_{DS(ON)}$
- ESD Protected Gate

Applications

- Logic Level Shift
- Load switch

Pin Configuration



Packing Information

| Device | Package | Reel Size | Quantity(Min. Package) |
|-----------|---------|-----------|------------------------|
| ECDJ1014E | SOT-563 | 7" | 3000pcs |

Absolute Maximum Ratings (at $T_A=25^\circ C$ Unless Otherwise Noted)

| Symbol | Parameter | N-Rating | P-Rating | Units |
|----------------|---|------------------|-------------|-------|
| V_{DS} | Drain-Source Voltage | 20 | -20 | V |
| V_{GS} | Gate-Source Voltage | ± 10 | ± 8 | V |
| I_D | Continuous Drain Current ^A | $T_A=25^\circ C$ | 0.65 | -0.55 |
| | | $T_A=70^\circ C$ | 0.5 | -0.45 |
| I_{DM} | Pulse Drain Current Tested ^B | 3 | -2.8 | A |
| P_D | Power Dissipation ^A | 0.15 | 0.15 | W |
| T_J, T_{STG} | Junction and Storage Temperature Range | -55 to +150 | -55 to +150 | °C |

Thermal Characteristics

| Symbol | Parameter | Typical | Units |
|-----------------|---|---------|-------|
| $R_{\theta JA}$ | Thermal Resistance-Junction to ambient ^A | 417 | °C/W |

N-Channel Electrical Characteristics (at $T_J = 25^\circ\text{C}$ Unless Otherwise Noted)

| Symbol | Parameter | Condition | Min. | Typ. | Max. | Units |
|-----------------------------------|---|--|------|------|----------|------------------|
| Static Parameters | | | | | | |
| BV_{DSS} | Drain-Source Breakdown Voltage | $\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$ | 20 | -- | -- | V |
| I_{DSS} | Zero Gate Voltage Drain Current | $\text{V}_{\text{DS}}=16\text{V}, \text{V}_{\text{GS}}=0\text{V}$ | -- | -- | 1 | μA |
| I_{GSS} | Gate-Body Leakage Current | $\text{V}_{\text{DS}}=0\text{V}, \text{V}_{\text{GS}}=\pm 10\text{V}$ | -- | -- | ± 30 | μA |
| $\text{V}_{\text{GS}(\text{th})}$ | Gate Threshold Voltage | $\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$ | 0.4 | -- | 1.0 | V |
| $\text{R}_{\text{DS}(\text{ON})}$ | Drain-Source On-State Resistance ^B | $\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=0.5\text{A}$ | -- | 250 | 380 | $\text{m}\Omega$ |
| | | $\text{V}_{\text{GS}}=2.5\text{V}, \text{I}_D=0.2\text{A}$ | -- | 300 | 440 | $\text{m}\Omega$ |
| | | $\text{V}_{\text{GS}}=1.8\text{V}, \text{I}_D=0.1\text{A}$ | -- | 370 | 530 | $\text{m}\Omega$ |
| V_{SD} | Diode Forward Voltage | $\text{I}_S=0.5\text{A}, \text{V}_{\text{GS}}=0\text{V}$ | -- | -- | 1.2 | V |
| Dynamic Parameters ^C | | | | | | |
| C_{iss} | Input Capacitance | $\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=10\text{V}$ $f=1\text{MHz}$ | -- | 67 | -- | pF |
| C_{oss} | Output Capacitance | | -- | 18 | -- | pF |
| C_{rss} | Reverse Transfer Capacitance | | -- | 6 | -- | pF |
| Q_g | Total Gate Charge | $\text{V}_{\text{DS}}=10\text{V}, \text{I}_D=0.5\text{A}$ $\text{V}_{\text{GS}}=4.5\text{V}$ | -- | 1.4 | -- | nC |
| Q_{gs} | Gate-Source Charge | | -- | 0.21 | -- | nC |
| Q_{gd} | Gate-Drain Charge | | -- | 0.21 | -- | nC |
| $t_{\text{D}(\text{on})}$ | Turn-on Delay Time | $\text{V}_{\text{DD}}=10\text{V}$ $\text{I}_D=0.15\text{A}, \text{R}_G=10\Omega,$ $\text{V}_{\text{GS}}=4.5\text{V}$ | -- | 2.8 | -- | ns |
| t_r | Turn-on Rise Time | | -- | 20 | -- | ns |
| $t_{\text{D}(\text{off})}$ | Turn-off Delay Time | | -- | 23 | -- | ns |
| t_f | Turn-off Fall Time | | -- | 24 | -- | ns |

A. The data tested by surface mounted on a 1 inch x 1 inch FR-4 board with 2OZ copper.

B. Pulse Test: Pulse Width $\leq 300\text{us}$, Duty cycle $\leq 2\%$.

C. Guaranteed by design, not subject to production testing.

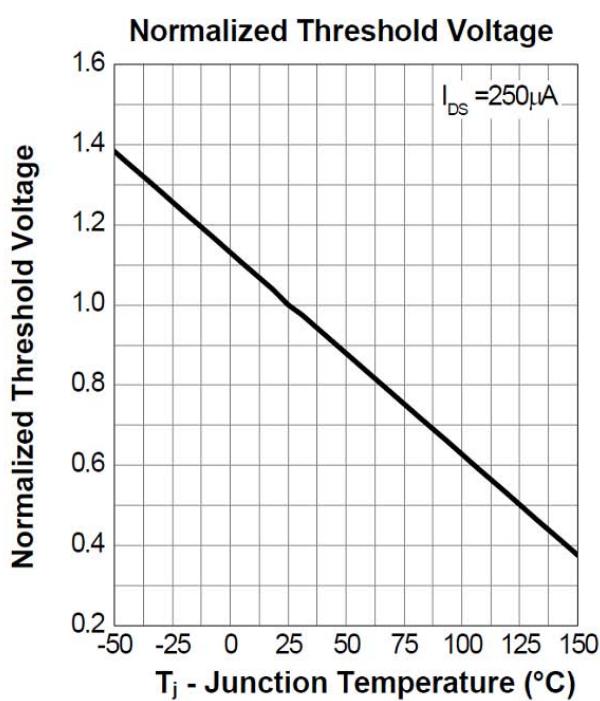
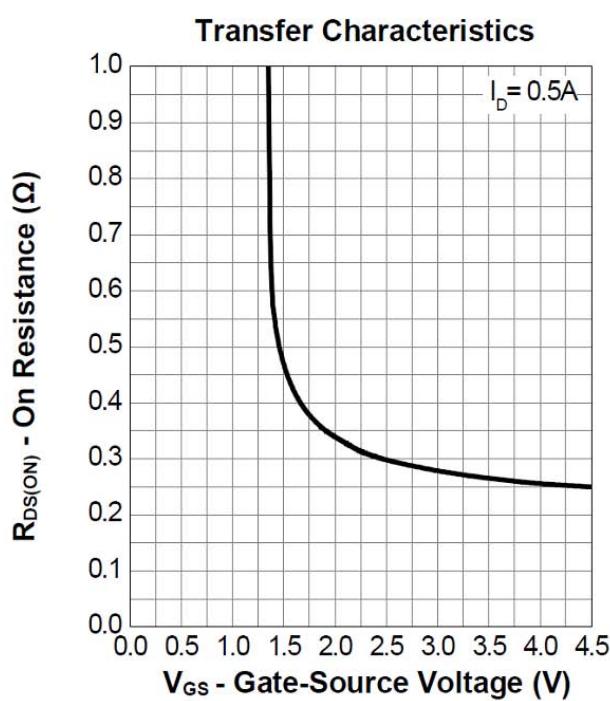
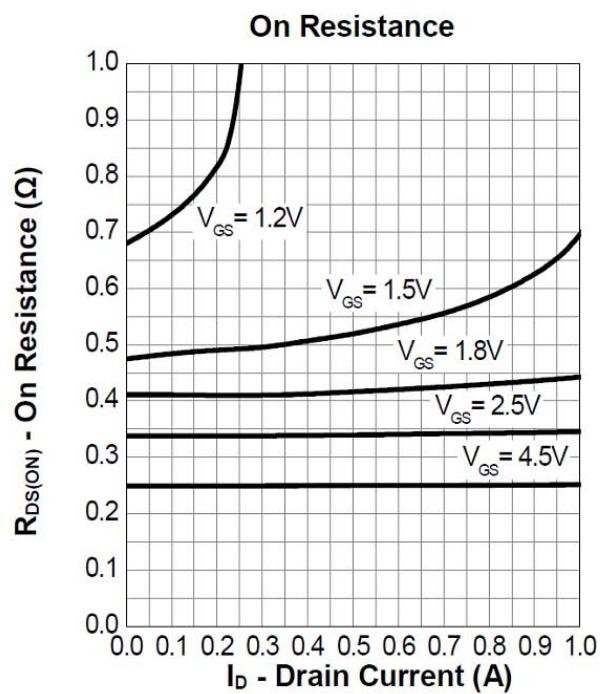
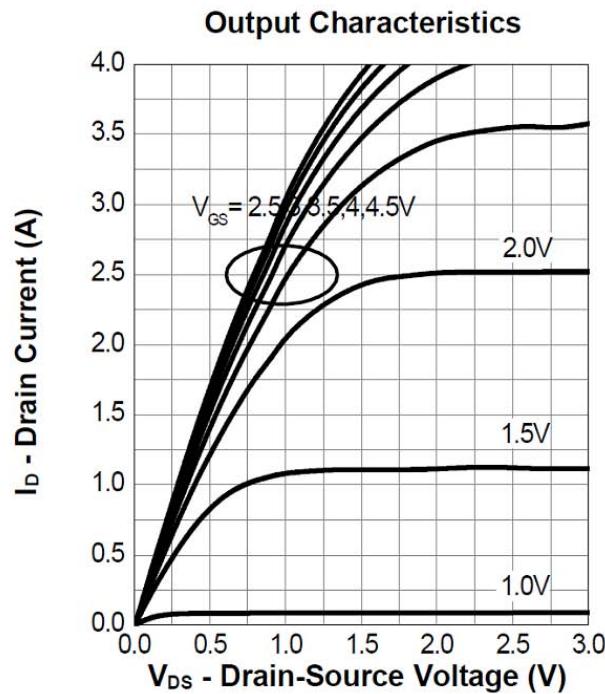
P-Channel Electrical Characteristics (at $T_J = 25^\circ\text{C}$ Unless Otherwise Noted)

| Symbol | Parameter | Condition | Min. | Typ. | Max. | Units |
|---------------------------------|---|---|-------|------|----------|------------------|
| Static Parameters | | | | | | |
| BV_{DSS} | Drain-Source Breakdown Voltage | $V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$ | -20 | -- | -- | V |
| I_{DSS} | Zero Gate Voltage Drain Current | $V_{\text{DS}}=-16\text{V}, V_{\text{GS}}=0\text{V}$ | -- | -- | -1 | μA |
| I_{GSS} | Gate-Body Leakage Current | $V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 6\text{V}$ | -- | -- | ± 10 | μA |
| $V_{\text{GS}(\text{th})}$ | Gate Threshold Voltage | $V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$ | -0.45 | -- | -1.2 | V |
| $R_{\text{DS}(\text{ON})}$ | Drain-Source On-State Resistance ^B | $V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-0.5\text{A}$ | -- | 350 | 480 | $\text{m}\Omega$ |
| | | $V_{\text{GS}}=-2.5\text{V}, I_{\text{D}}=-0.3\text{A}$ | -- | 440 | 670 | $\text{m}\Omega$ |
| | | $V_{\text{GS}}=-1.8\text{V}, I_{\text{D}}=-0.1\text{A}$ | -- | 780 | -- | $\text{m}\Omega$ |
| V_{SD} | Diode Forward Voltage | $I_{\text{S}}=-0.35\text{A}, V_{\text{GS}}=0\text{V}$ | -- | -- | -1.2 | V |
| Dynamic Parameters ^C | | | | | | |
| C_{iss} | Input Capacitance | $V_{\text{GS}}=0\text{V}, V_{\text{DS}}=-16\text{V}$ $f=1\text{MHz}$ | -- | 152 | -- | pF |
| C_{oss} | Output Capacitance | | -- | 18 | -- | pF |
| C_{rss} | Reverse Transfer Capacitance | | -- | 6 | -- | pF |
| Q_g | Total Gate Charge | $V_{\text{DS}}=-16\text{V}, I_{\text{D}}=-0.2\text{A}$ $V_{\text{GS}}=-4.5\text{V}$ | -- | 2.8 | -- | nC |
| Q_{gs} | Gate-Source Charge | | -- | 2.1 | -- | nC |
| Q_{gd} | Gate-Drain Charge | | -- | 0.5 | -- | nC |
| $t_{\text{D}(\text{on})}$ | Turn-on Delay Time | $V_{\text{DD}}=-10\text{V}$ $I_{\text{D}}=-0.2\text{A}, R_{\text{G}}=10\Omega$, $V_{\text{GS}}=-4.5\text{V}$ | -- | 51.3 | -- | ns |
| t_r | Turn-on Rise Time | | -- | 24.2 | -- | ns |
| $t_{\text{D}(\text{off})}$ | Turn-off Delay Time | | -- | 246 | -- | ns |
| t_f | Turn-off Fall Time | | -- | 81.2 | -- | ns |

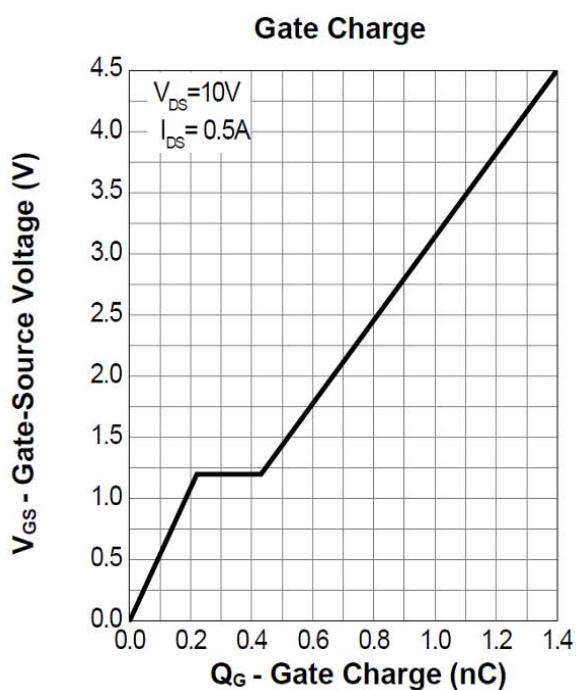
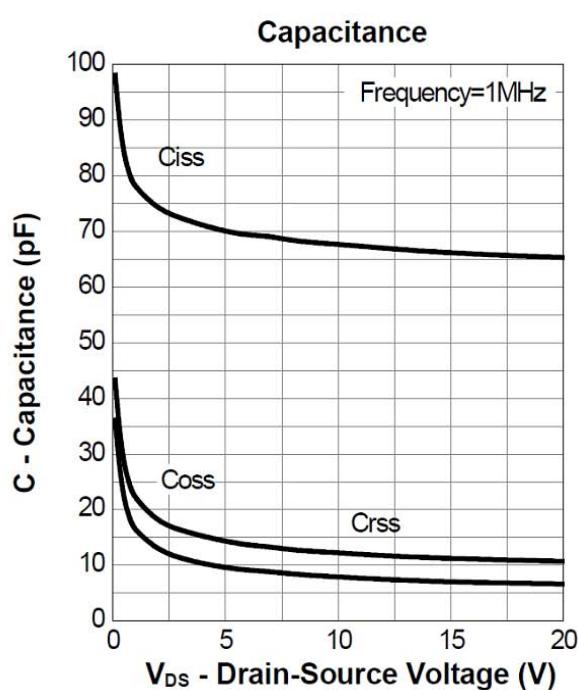
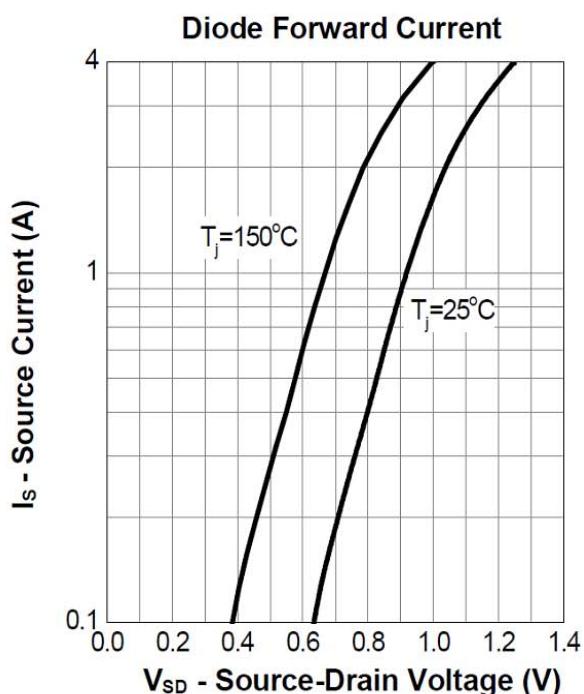
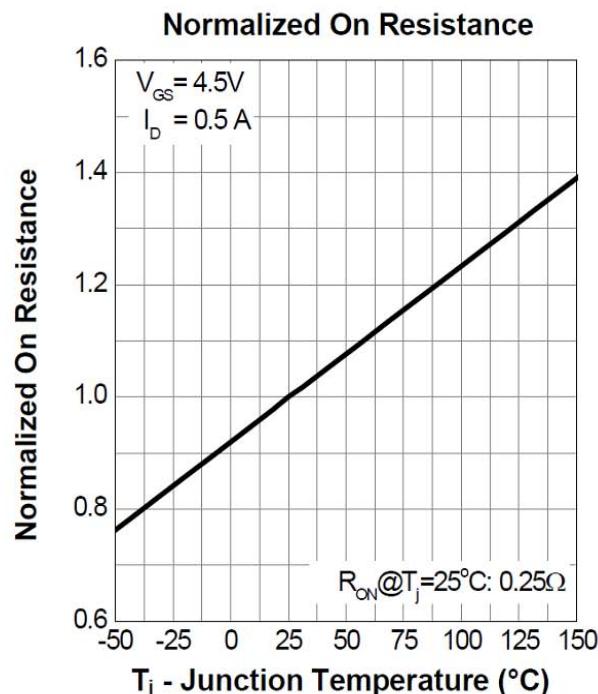
B.Pulse Test: Pulse Width $\leq 300\text{us}$, Duty cycle $\leq 2\%$.

C. Guaranteed by design, not subject to production testing.

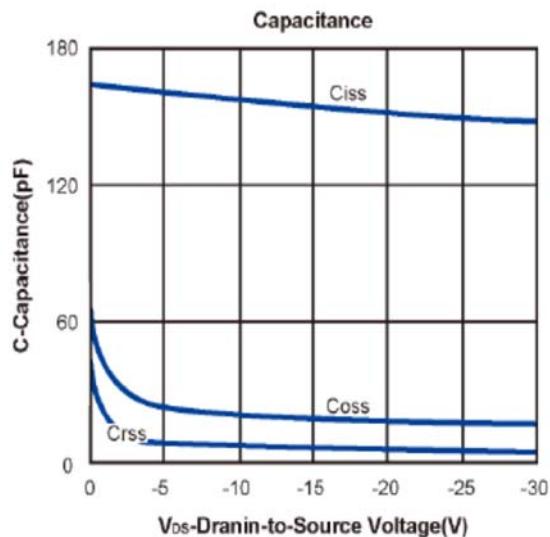
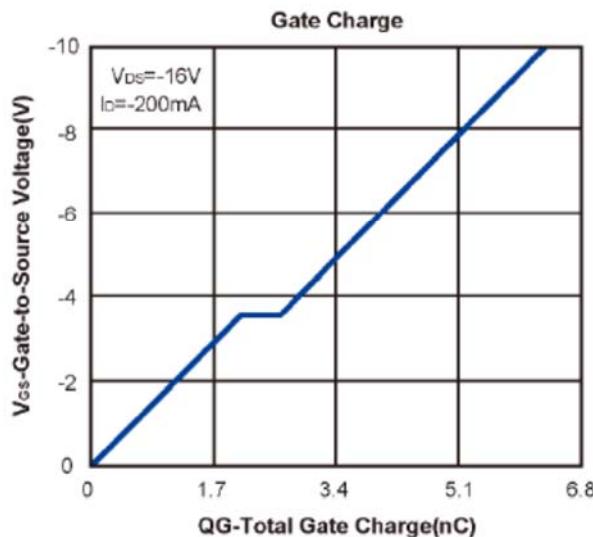
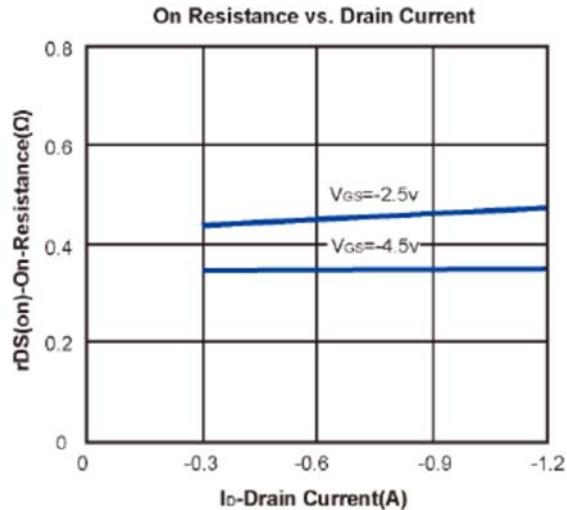
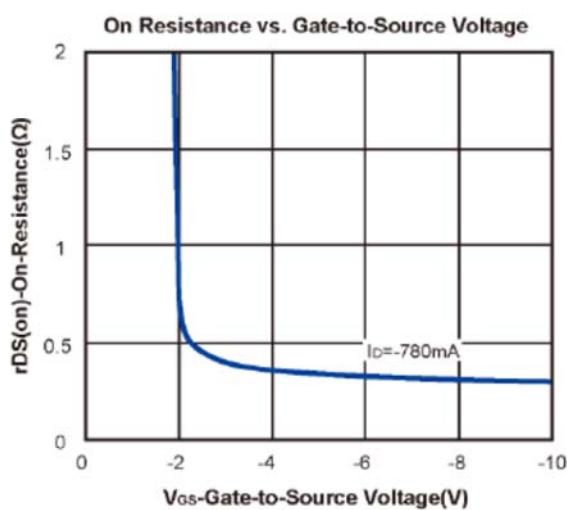
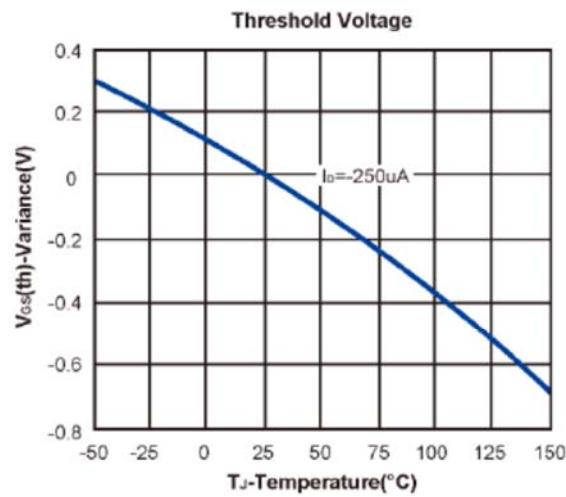
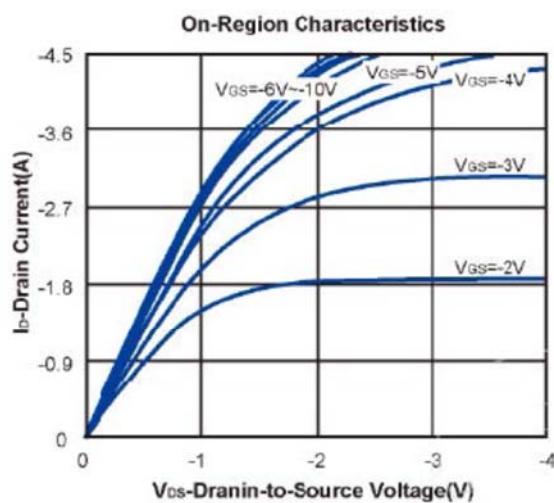
N-Channel Typical Characteristics



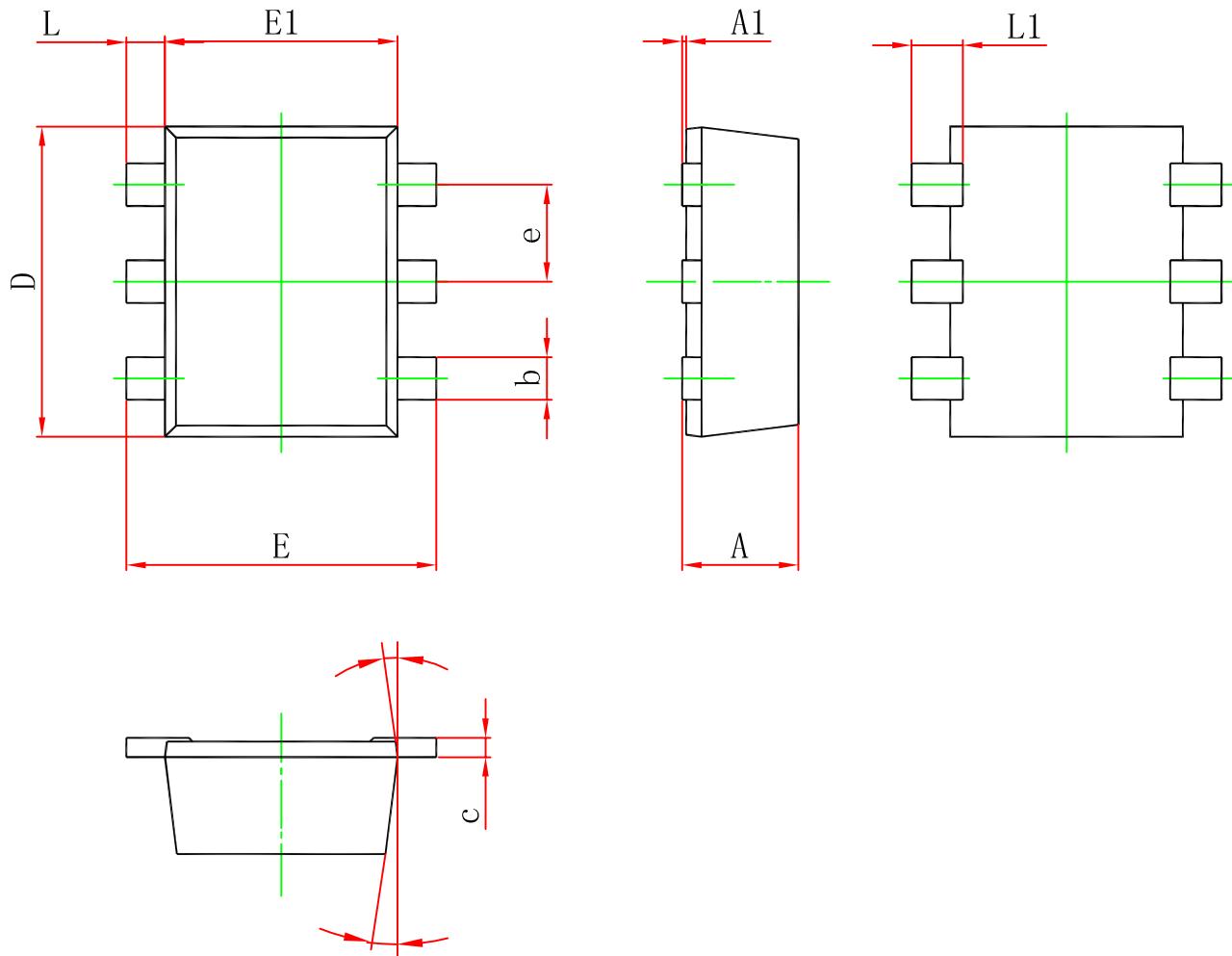
N-Channel Typical Characteristics



P-Channel Typical Characteristics



SOT-563 Package Information



| Symbol | Dimensions In Millimeters | | Dimensions in inches | |
|--------|---------------------------|-------|----------------------|-------|
| | Min. | Max. | Min. | Max. |
| A | 0.525 | 0.600 | 0.021 | 0.024 |
| A1 | 0.000 | 0.050 | 0.000 | 0.002 |
| e | 0.450 | 0.550 | 0.018 | 0.022 |
| c | 0.090 | 0.160 | 0.004 | 0.006 |
| D | 1.500 | 1.700 | 0.059 | 0.067 |
| b | 0.170 | 0.270 | 0.007 | 0.011 |
| E1 | 1.100 | 1.300 | 0.043 | 0.051 |
| E | 1.500 | 1.700 | 0.059 | 0.067 |
| L | 0.100 | 0.300 | 0.004 | 0.012 |
| L1 | 0.200 | 0.400 | 0.008 | 0.016 |
| 0 | 7 ⁰ REF. | | 7 ⁰ REF. | |