

N-Channel and P-Channel 20V(D-S) MOSFET

| Product summary | | | |
|--|------|------|----|
| V _{DS} | 20 | -20 | V |
| R _{DS(ON)} (at V _{GS} =4.5V) Typ. | 19.5 | 42 | mΩ |
| R _{DS(ON)} (at V _{GS} =2.5V) Typ. | 25 | 55 | mΩ |
| I _D (T _A =25°C) | 4.5 | -3.4 | A |

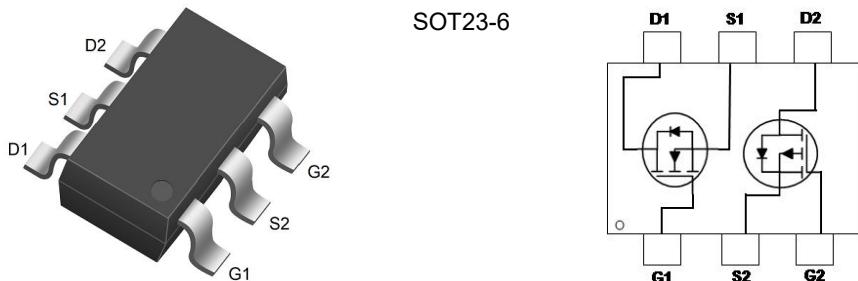
Features

- Super Low Gate Charge
- Trench Power LV MOSFET technology

Applications

- Power management functions
- Load switch

Pin Configuration



Packing Information

| Device | Package | Reel Size | Quantity(Min. Package) |
|------------|---------|-----------|------------------------|
| ECDE04C02C | SOT23-6 | 7" | 3000pcs |

Absolute Maximum Ratings (at T_A=25°C Unless Otherwise Noted)

| Symbol | Parameter | N-Rating | P-Rating | Units |
|-----------------------------------|---|----------------------|-------------|-------|
| V _{DS} | Drain-Source Voltage | 20 | -20 | V |
| V _{GS} | Gate-Source Voltage | ±10 | ±10 | V |
| I _D | Continuous Drain Current ^A | T _A =25°C | 4.5 | -3.4 |
| | | T _A =70°C | 3.6 | -2.7 |
| I _{DM} | Pulse Drain Current Tested ^B | 18 | -14 | A |
| P _D | Power Dissipation ^A | 1.2 | 1.2 | W |
| T _J , T _{STG} | Junction and Storage Temperature Range | -55 to +150 | -55 to +150 | °C |

Thermal Characteristics

| Symbol | Parameter | Typical | Units |
|------------------|---|---------|-------|
| R _{θJA} | Thermal Resistance-Junction to ambient ^A | 104 | °C/W |

N-Channel Electrical Characteristics (at $T_J = 25^\circ\text{C}$ Unless Otherwise Noted)

| Symbol | Parameter | Condition | Min. | Typ. | Max. | Units |
|--|---|--|------|------|-----------|------------------|
| Static Parameters | | | | | | |
| BV_{DSS} | Drain-Source Breakdown Voltage | $\text{V}_{\text{GS}}=0\text{V}, \text{I}_{\text{D}}=250\mu\text{A}$ | 20 | -- | -- | V |
| I_{DSS} | Zero Gate Voltage Drain Current | $\text{V}_{\text{DS}}=20\text{V}, \text{V}_{\text{GS}}=0\text{V}$ | -- | -- | 1 | μA |
| I_{GSS} | Gate-Body Leakage Current | $\text{V}_{\text{DS}}=0\text{V}, \text{V}_{\text{GS}}=\pm 10\text{V}$ | -- | -- | ± 100 | nA |
| $\text{V}_{\text{GS}(\text{th})}$ | Gate Threshold Voltage | $\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_{\text{D}}=250\mu\text{A}$ | 0.45 | 0.63 | 1.0 | V |
| $\text{R}_{\text{DS}(\text{ON})}$ | Drain-Source On-State Resistance ^B | $\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_{\text{D}}=4.5\text{A}$ | -- | 19.5 | 25 | $\text{m}\Omega$ |
| | | $\text{V}_{\text{GS}}=2.5\text{V}, \text{I}_{\text{D}}=3\text{A}$ | -- | 25 | 32 | $\text{m}\Omega$ |
| | | $\text{V}_{\text{GS}}=1.8\text{V}, \text{I}_{\text{D}}=2.7\text{A}$ | -- | 33 | 49 | $\text{m}\Omega$ |
| V_{SD} | Diode Forward Voltage | $\text{I}_{\text{S}}=4.5\text{A}, \text{V}_{\text{GS}}=0\text{V}$ | -- | -- | 1.2 | V |
| I_{S} | Maximum Body-Diode Continuous Current | | -- | -- | 4.5 | A |
| Dynamic Parameters ^C | | | | | | |
| C_{iss} | Input Capacitance | $\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=10\text{V}$ $f=1\text{MHz}$ | -- | 620 | -- | pF |
| C_{oss} | Output Capacitance | | -- | 114 | -- | pF |
| C_{rss} | Reverse Transfer Capacitance | | -- | 64 | -- | pF |
| Q_{g} | Total Gate Charge | $\text{V}_{\text{DS}}=10\text{V}, \text{I}_{\text{D}}=4.5\text{A}$ $\text{V}_{\text{GS}}=4.5\text{V}$ | -- | 7.1 | -- | nC |
| Q_{gs} | Gate-Source Charge | | -- | 1.4 | -- | nC |
| Q_{gd} | Gate-Drain Charge | | -- | 1.9 | -- | nC |
| $t_{\text{D}(\text{on})}$ | Turn-on Delay Time | $\text{V}_{\text{DD}}=10\text{V}$ $\text{R}_{\text{L}}=1.5\Omega, \text{R}_{\text{G}}=3\Omega,$ $\text{V}_{\text{GS}}=4.5\text{V}$ | -- | 13 | -- | nS |
| t_{r} | Turn-on Rise Time | | -- | 54 | -- | nS |
| $t_{\text{D}(\text{off})}$ | Turn-off Delay Time | | -- | 18 | -- | nS |
| t_{f} | Turn-off Fall Time | | -- | 11 | -- | nS |

A. The data tested by surface mounted on a 1 inch x 1 inch FR-4 board with 2OZ copper.

B. Pulse Test: Pulse Width $\leq 300\text{us}$, Duty cycle $\leq 2\%$.

C. Guaranteed by design, not subject to production testing.

P-Channel Electrical Characteristics (at $T_J = 25^\circ\text{C}$ Unless Otherwise Noted)

| Symbol | Parameter | Condition | Min. | Typ. | Max. | Units |
|---------------------------------|---|--|------|-------|-----------|------------------|
| Static Parameters | | | | | | |
| BV_{DSS} | Drain-Source Breakdown Voltage | $V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$ | -20 | -- | -- | V |
| I_{DSS} | Zero Gate Voltage Drain Current | $V_{\text{DS}}=-20\text{V}, V_{\text{GS}}=0\text{V}$ | -- | -- | -1 | μA |
| I_{GSS} | Gate-Body Leakage Current | $V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 10\text{V}$ | -- | -- | ± 100 | nA |
| $V_{\text{GS}(\text{th})}$ | Gate Threshold Voltage | $V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$ | -0.4 | -0.63 | -1.0 | V |
| $R_{\text{DS}(\text{ON})}$ | Drain-Source On-State Resistance ^B | $V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-3.4\text{A}$ | -- | 42 | 60 | $\text{m}\Omega$ |
| | | $V_{\text{GS}}=-2.5\text{V}, I_{\text{D}}=-3\text{A}$ | -- | 55 | 75 | $\text{m}\Omega$ |
| | | $V_{\text{GS}}=-1.8\text{V}, I_{\text{D}}=-2.5\text{A}$ | -- | 74 | 110 | $\text{m}\Omega$ |
| V_{SD} | Diode Forward Voltage | $I_{\text{S}}=-3.4\text{A}, V_{\text{GS}}=0\text{V}$ | -- | -- | -1.2 | V |
| I_{SM} | Maximum Body-Diode Continuous Current | | -- | -- | -3.4 | A |
| Dynamic Parameters ^C | | | | | | |
| C_{iss} | Input Capacitance | $V_{\text{GS}}=0\text{V}, V_{\text{DS}}=-10\text{V}$ $f=1\text{MHZ}$ | -- | 550 | -- | pF |
| C_{oss} | Output Capacitance | | -- | 89 | -- | pF |
| C_{rss} | Reverse Transfer Capacitance | | -- | 65 | -- | pF |
| Q_{g} | Total Gate Charge | $V_{\text{DS}}=-10\text{V}, I_{\text{D}}=-3.4\text{A}$ $V_{\text{GS}}=-4.5\text{V}$ | -- | 4.3 | -- | nC |
| Q_{gs} | Gate-Source Charge | | -- | 0.8 | -- | nC |
| Q_{gd} | Gate-Drain Charge | | -- | 1.1 | -- | nC |
| $t_{\text{D}(\text{on})}$ | Turn-on Delay Time | $V_{\text{DD}}=-10\text{V}$ $I_{\text{D}}=-1\text{A}, R_{\text{G}}=2.5\Omega$, $V_{\text{GS}}=-4.5\text{V}$ | -- | 12 | -- | nS |
| t_{r} | Turn-on Rise Time | | -- | 54 | -- | nS |
| $t_{\text{D}(\text{off})}$ | Turn-off Delay Time | | -- | 15 | -- | nS |
| t_{f} | Turn-off Fall Time | | -- | 9 | -- | nS |

B.Pulse Test: Pulse Width $\leq 300\text{us}$, Duty cycle $\leq 2\%$.

C. Guaranteed by design, not subject to production testing.

N-Channel Typical Characteristics

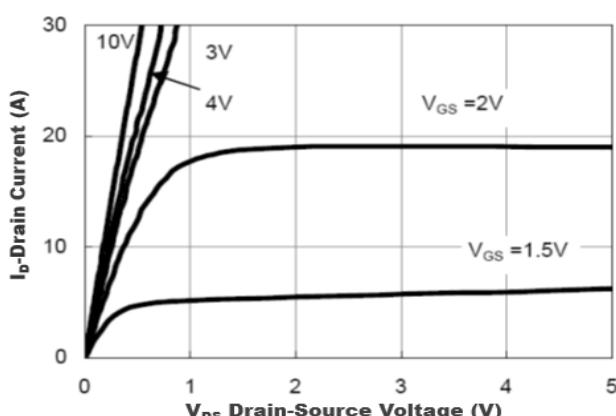


Figure1. Output Characteristics

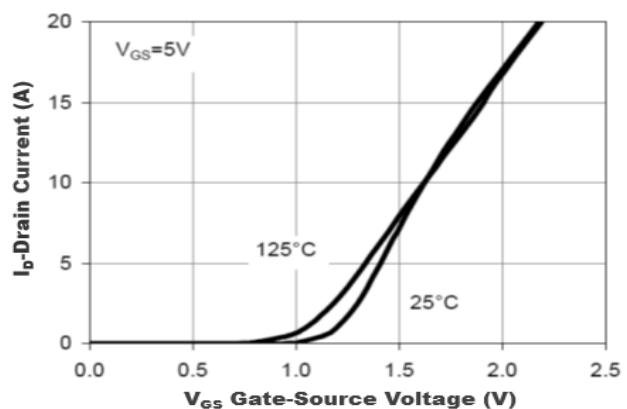


Figure2. Transfer Characteristics

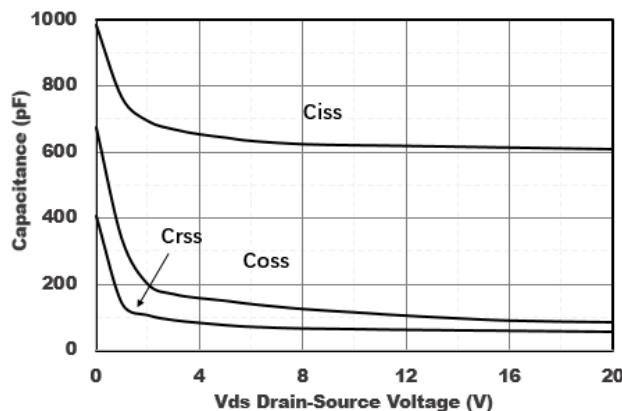


Figure3. Capacitance Characteristics

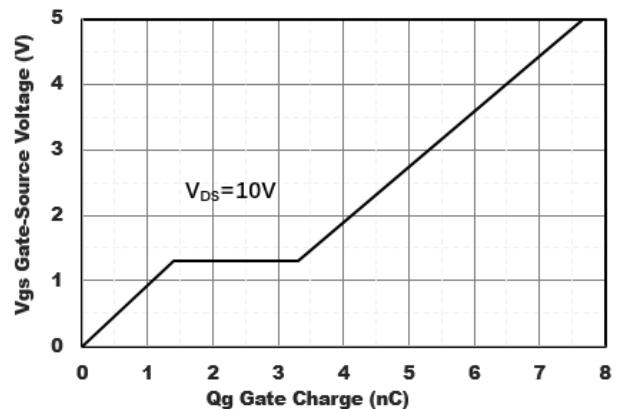


Figure4. Gate Charge

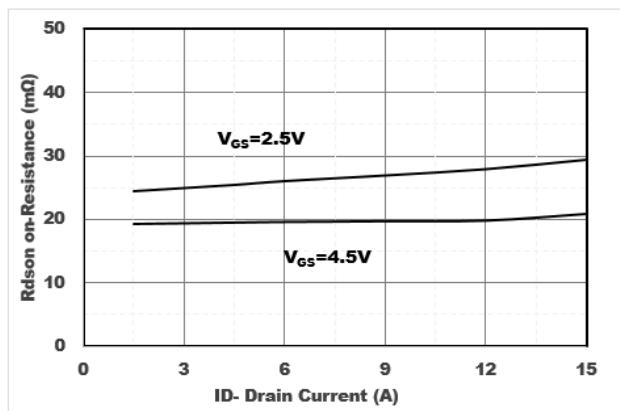


Figure5. Drain-Source on Resistance

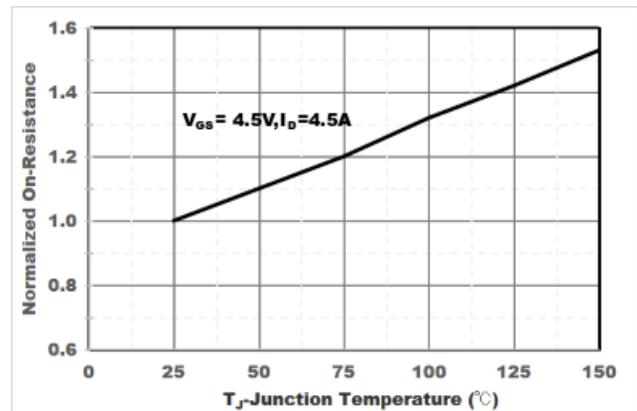


Figure6. Drain-Source on Resistance

N-Channel Typical Characteristics

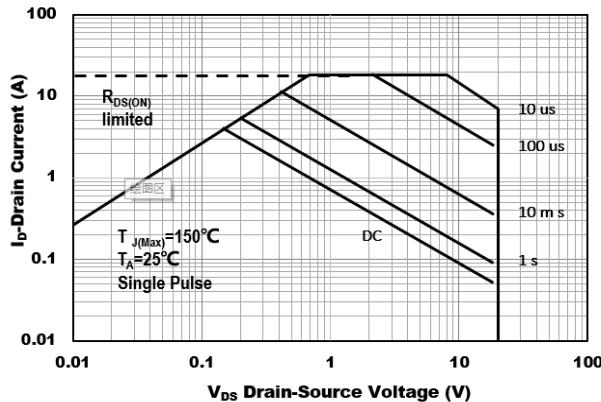


Figure7. Safe Operation Area

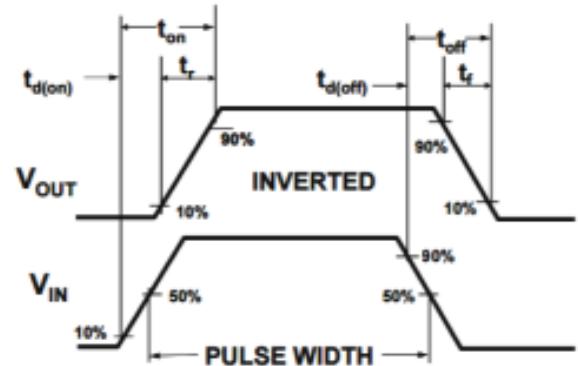


Figure8. Switching wave

P-Channel Typical Characteristics

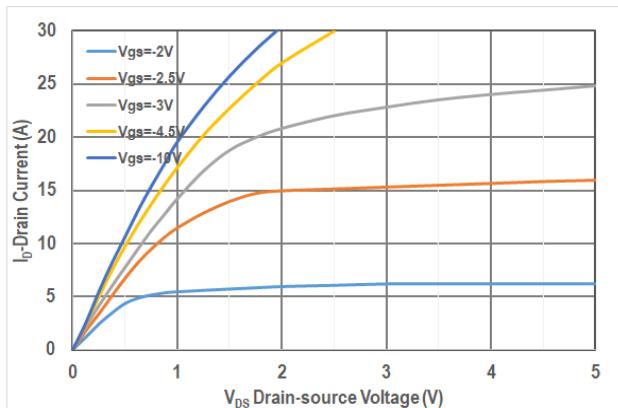


Figure1. Output Characteristics

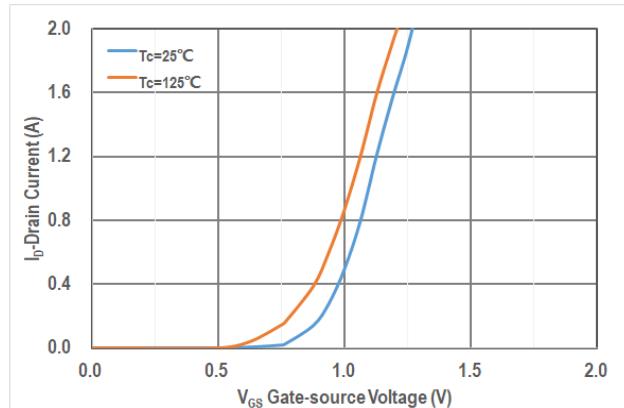


Figure2. Transfer Characteristics

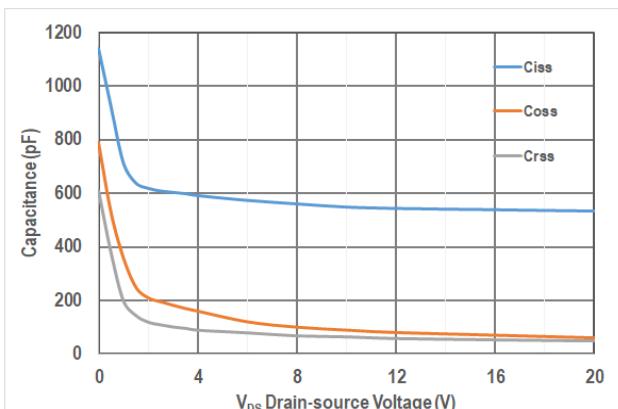


Figure3. Capacitance Characteristics

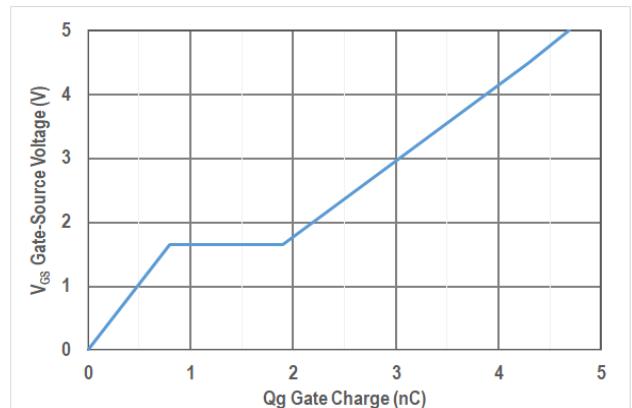


Figure4. Gate Charge

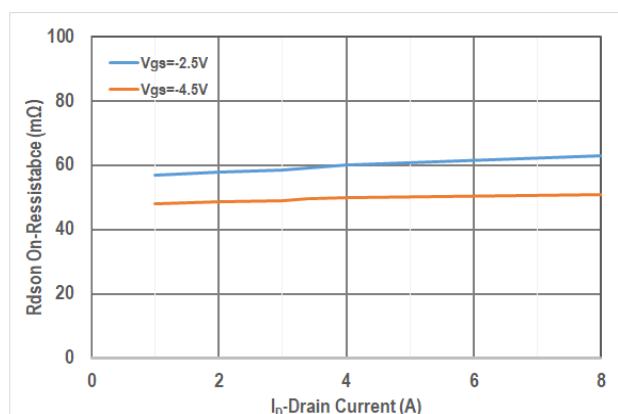


Figure5. Drain-Source on Resistance

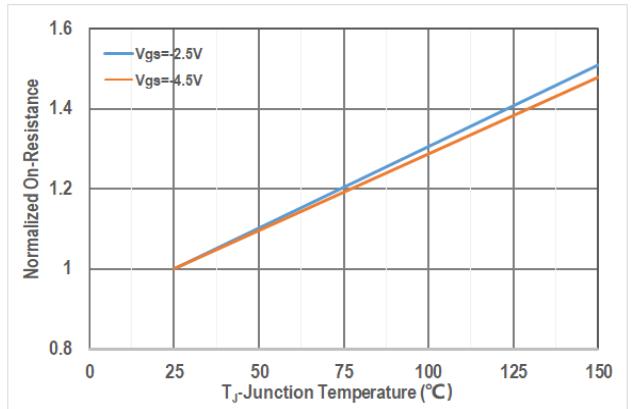


Figure6. Drain-Source on Resistance

P-Channel Typical Characteristics

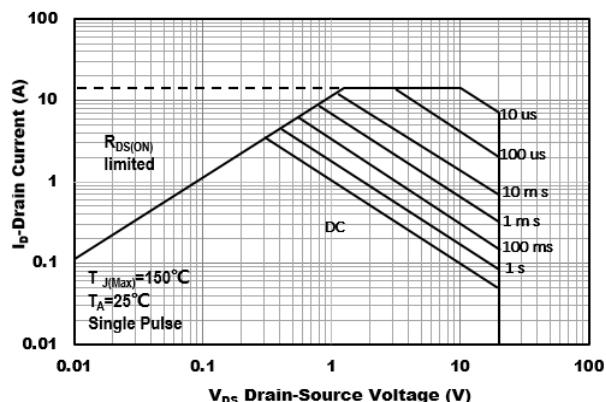


Figure7. Safe Operation Area

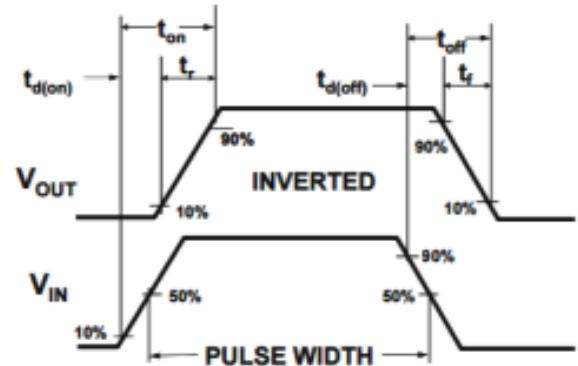
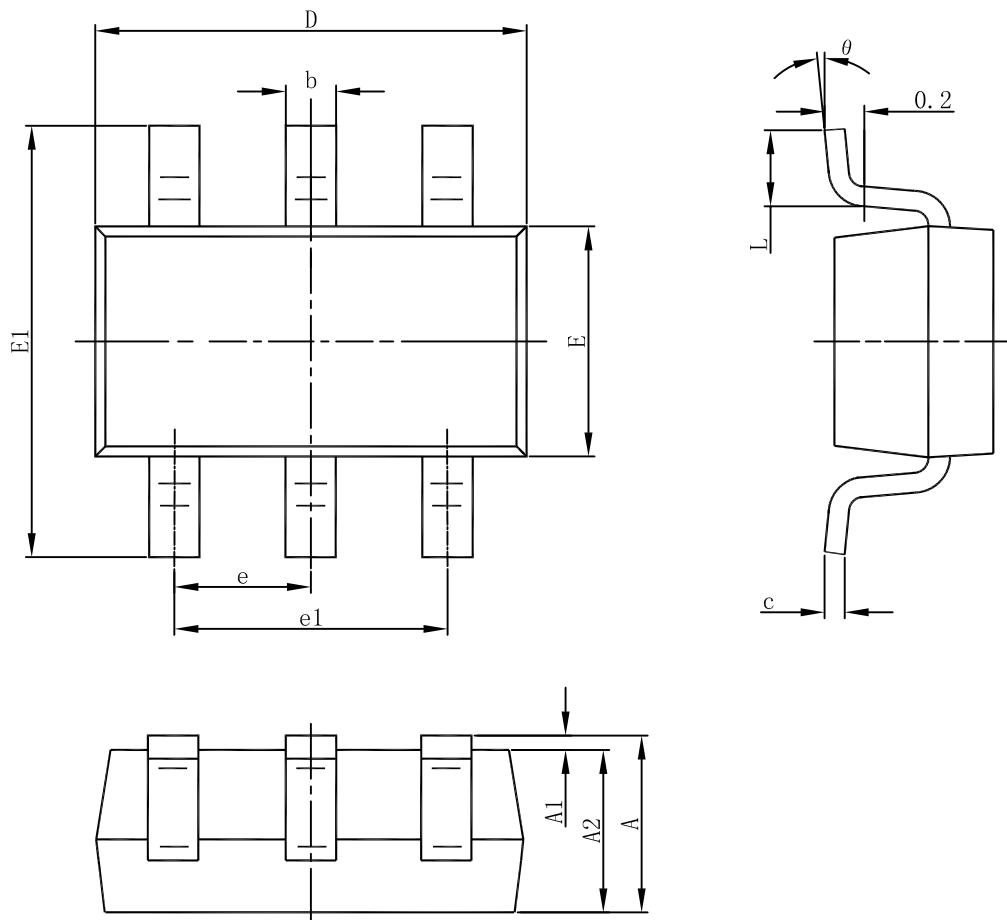


Figure8. Switching wave

SOT23-6 Package Information



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|--------|---------------------------|-------|----------------------|-------|
| | Min | Max | Min | Max |
| A | 1.050 | 1.250 | 0.041 | 0.049 |
| A1 | 0.000 | 0.100 | 0.000 | 0.004 |
| A2 | 1.050 | 1.150 | 0.041 | 0.045 |
| b | 0.300 | 0.500 | 0.012 | 0.020 |
| c | 0.100 | 0.200 | 0.004 | 0.008 |
| D | 2.820 | 3.020 | 0.111 | 0.119 |
| E | 1.500 | 1.700 | 0.059 | 0.067 |
| E1 | 2.650 | 2.950 | 0.104 | 0.116 |
| e | 0.950(BSC) | | 0.037(BSC) | |
| e1 | 1.800 | 2.000 | 0.071 | 0.079 |
| L | 0.300 | 0.600 | 0.012 | 0.024 |
| theta | 0° | 8° | 0° | 8° |