

N-Channel 60V(D-S) MOSFET

Product summary

V_{DS}	60	V
$R_{DS(ON)}$ (at $V_{GS}=10V$) Typ.	3.9	m Ω
$R_{DS(ON)}$ (at $V_{GS}=4.5V$) Typ.	5.0	m Ω
I_D ($T_C=25^\circ C$)	98	A

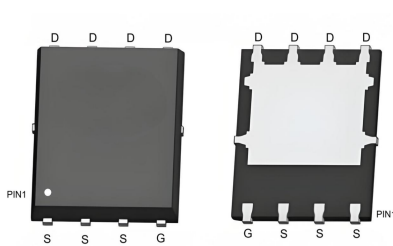
Features

- High Current Capability
- Low Gate Charge
- Ultra-low $R_{DS(ON)}$

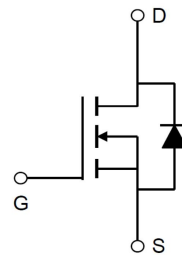
Applications

- Power management functions
- Load switching

Pin Configuration



PDFN5X6-8L



Packing Information

Device	Package	Reel Size	Quantity(Min. Package)
ECAP98N06A	PDFN5X6-8L	13"	3000pcs

Absolute Maximum Ratings (at $T_A=25^\circ C$ Unless Otherwise Noted)

Symbol	Parameter		Rating	Units
V_{DS}	Drain-Source Voltage		60	V
V_{GS}	Gate-Source Voltage		± 20	V
I_D	Continuous Drain Current ^A	$T_C=25^\circ C$	98	A
		$T_C=100^\circ C$	62	A
I_{DM}	Pulse Drain Current Tested ^B		360	A
I_{AS}	Avalanche Current ^C		27	A
E_{AS}	Single Pulse Avalanche Energy ^C		109	mJ
P_D	Power Dissipation @ $T_C=25^\circ C$ ^D		78	W
T_J, T_{STG}	Junction and Storage Temperature Range		-55 to +150	$^\circ C$

Thermal Characteristics

Symbol	Parameter	Typical	Units
$R_{\theta JC}$	Thermal Resistance-Junction to case max	1.6	$^\circ C/W$

Electrical Characteristics (at $T_J = 25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Condition	Min.	Typ.	Max.	Units
Static Parameters						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	60	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=48V, V_{GS}=0V$	--	--	1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{DS}=0V, V_{GS}=\pm 20V$	--	--	± 100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.2	1.8	2.5	V
$R_{DS(ON)}$	Drain-Source On-State Resistance ^C	$V_{GS}=10V, I_D=20A$	--	3.9	4.9	m Ω
		$V_{GS}=4.5V, I_D=15A$	--	5.0	6.3	m Ω
V_{SD}	Diode Forward Voltage	$I_S=1A, V_{GS}=0V$	--	--	1.0	V
g_{FS}	Forward Transconductance	$V_{DS}=5V, I_D=20A$	--	121	--	S
I_S	Diode Continuous Current	$T_C=25^\circ\text{C}$	--	--	121	A
R_g	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, f=1\text{MHz}$	--	1.78	--	Ω
Dynamic Parameters ^E						
C_{iss}	Input Capacitance	$V_{GS}=0V, V_{DS}=30V$ $f=1\text{MHz}$	--	2030	--	pF
C_{oss}	Output Capacitance		--	445	--	pF
C_{rss}	Reverse Transfer Capacitance		--	4.4	--	pF
Q_g	Total Gate Charge (@ $V_{GS}=10V$)	$V_{DS}=30V, I_D=20A$ $V_{GS}=0$ to $10V$	--	32	--	nC
Q_g	Total Gate Charge (@ $V_{GS}=4.5V$)		--	14.9	--	nC
Q_{gs}	Gate-Source Charge		--	4.4	--	nC
Q_{gd}	Gate-Drain Charge		--	4.9	--	nC
$t_{D(on)}$	Turn-on Delay Time	$V_{DS}=30V$ $R_{GEN}=6\Omega,$ $R_L=1.5\Omega,$ $V_{GS}=10V$	--	6.3	--	ns
t_r	Turn-on Rise Time		--	7.8	--	ns
$t_{D(off)}$	Turn-off Delay Time		--	39	--	ns
t_f	Turn-off Fall Time		--	15.5	--	ns
t_{rr}	Reverse recovery time	$I_F=20A,$ $di/dt=100\text{ A}/\mu\text{S}$	--	39	--	ns
Q_{rr}	Reverse recovery charge		--	45	--	μC

A. Computed continuous current assumes the condition of T_{J_Max} while the actual continuous current depends on the thermal & electro-mechanical application board design.

B. This single-pulse measurement was taken under $T_{J_Max}=150^\circ\text{C}$.

C. This single-pulse measurement was taken under the following condition [$L=300\mu\text{H}$, $V_{GS}=10V$, $V_{DS}=30V$] while its value is limited by $T_{J_Max}=150^\circ\text{C}$.

D. The power dissipation PD is based on $T_{J_Max}=150^\circ\text{C}$.

E. Guaranteed by design, not subject to production testing.

F. Continuous current rating is limited by the package used.

Typical Characteristics

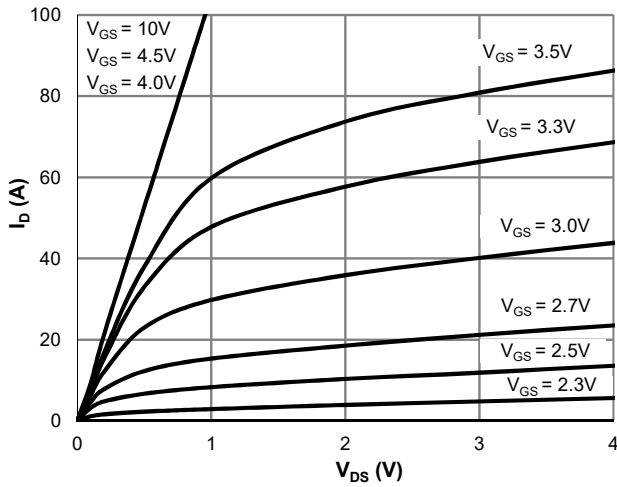


Figure 1: Saturation Characteristics

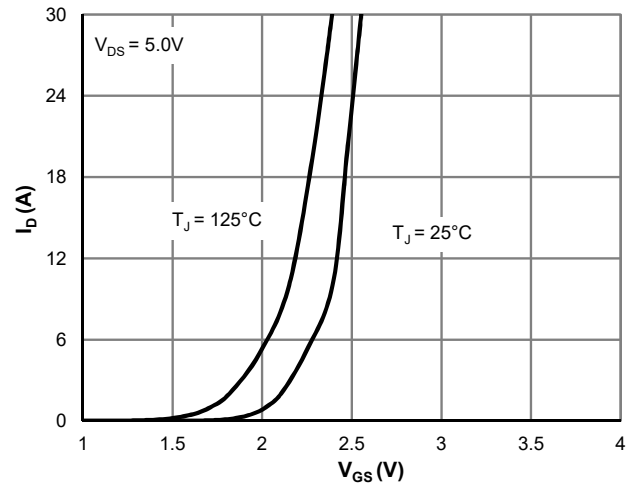


Figure 2: Transfer Characteristics

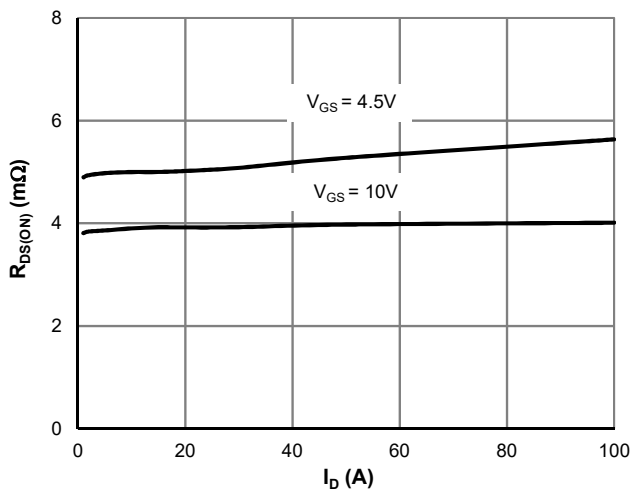


Figure 3: $R_{DS(ON)}$ vs. Drain Current

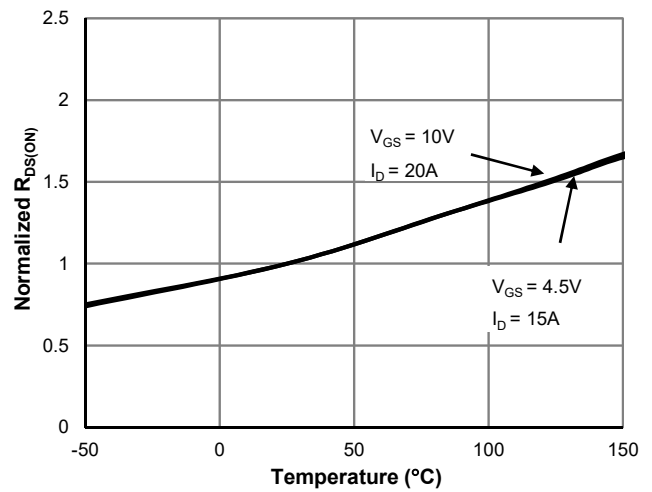


Figure 4: $R_{DS(ON)}$ vs. Junction Temperature

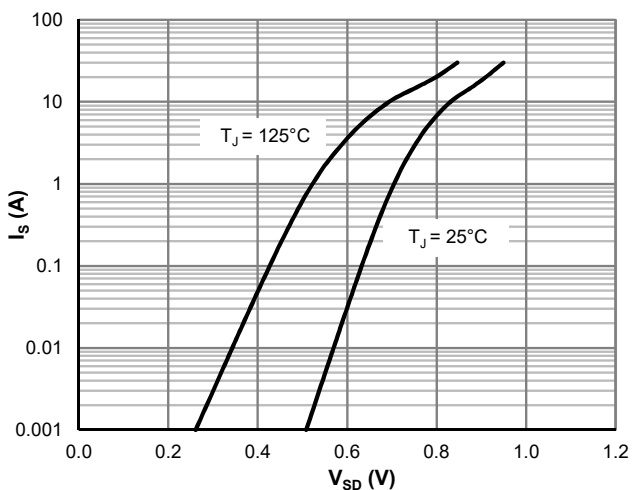


Figure 5: Body-Diode Characteristics

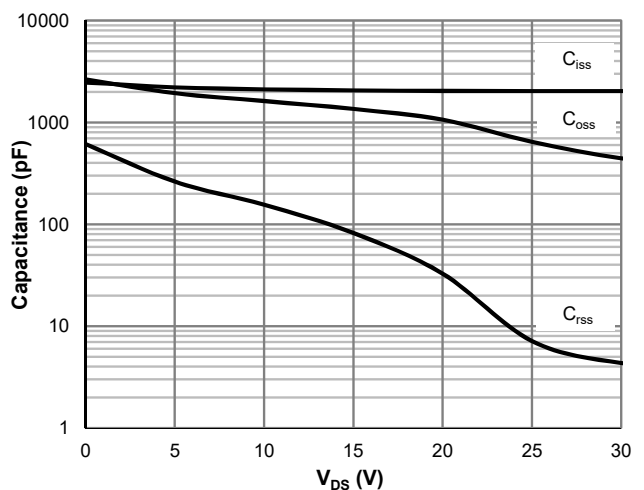


Figure 6: Capacitance Characteristics

Typical Characteristics

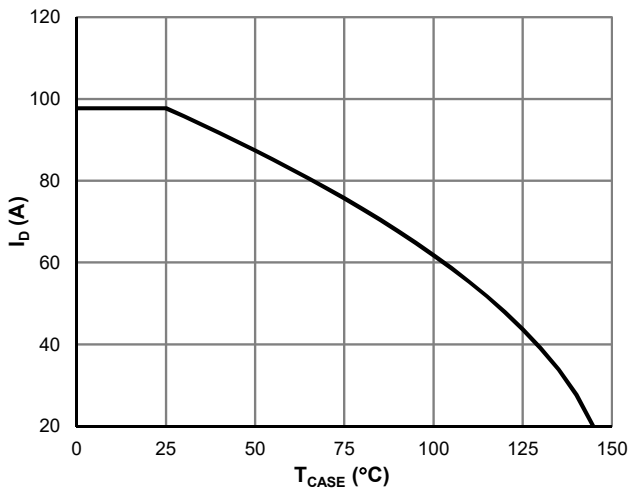


Figure 7: Current De-rating

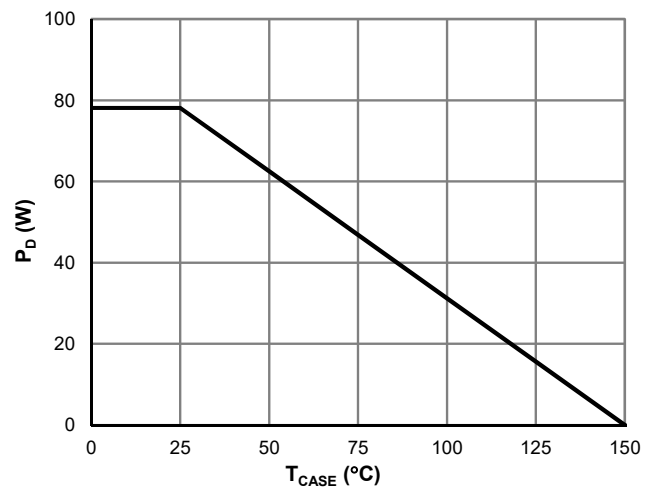


Figure 8: Power De-rating

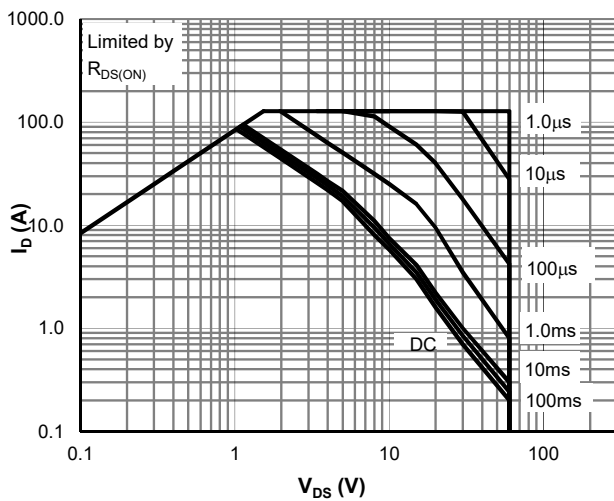


Figure 9: Maximum Safe Operating Area

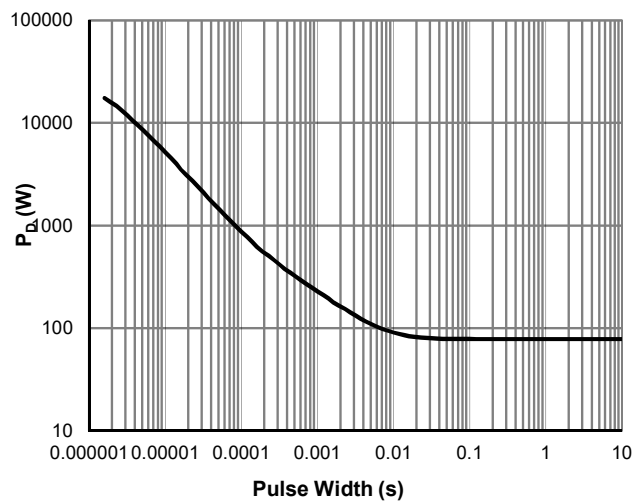


Figure 10: Single Pulse Power Rating, Junction-to-Case

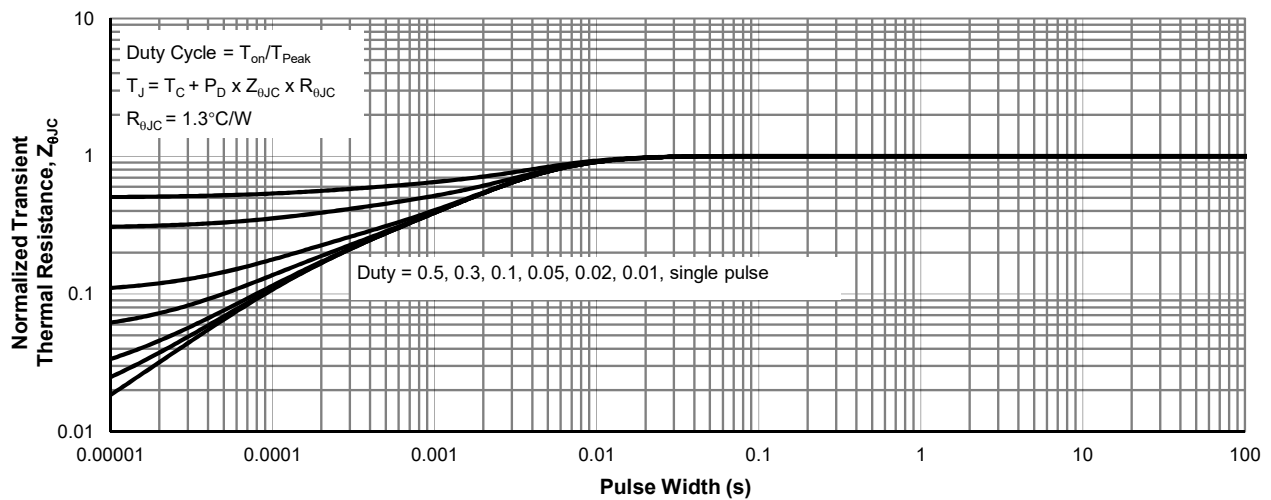
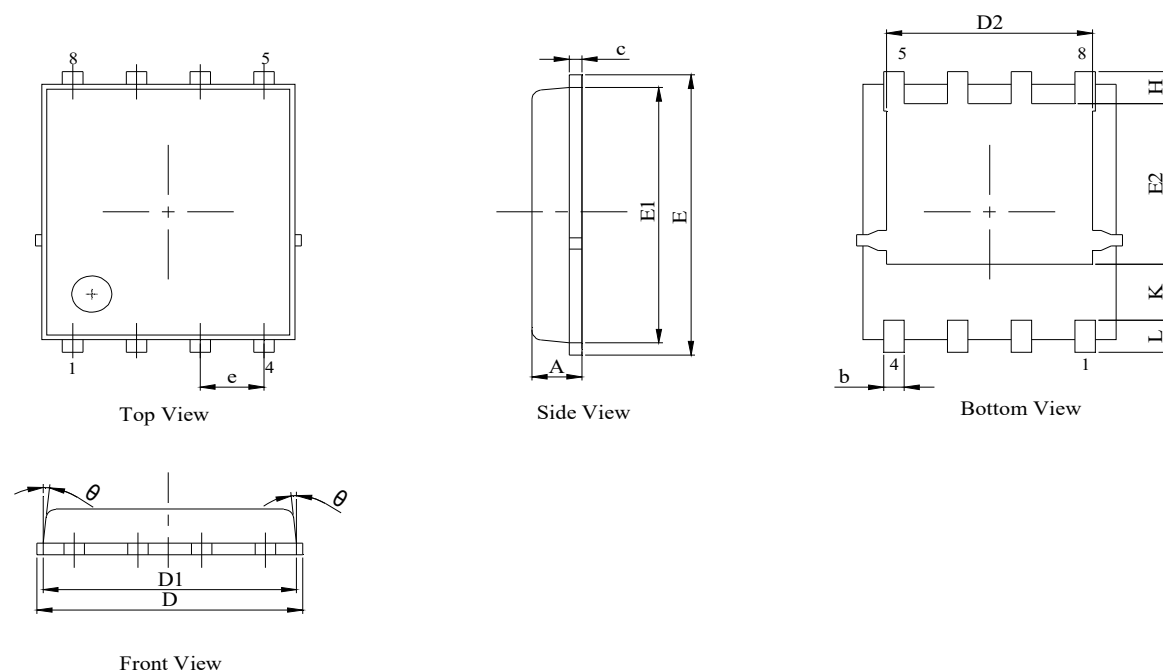


Figure 11: Normalized Maximum Transient Thermal Impedance

PDFN5X6-8L Package Information (unit:mm)



NOTES:

1. Dimension and tolerance per ASME Y14.5M, 1994.
2. All dimensions in millimeter (angle in degree).
3. Dimensions $D1$ and $E1$ do not include mold flash protrusions or gate burrs.

DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
b	0.31	0.41	0.51
c	0.20	0.25	0.30
D	5.00	5.20	5.40
D1	4.95	5.05	5.15
D2	4.00	4.10	4.20
E	6.05	6.15	6.25
E1	5.50	5.60	5.70
E2	3.42	3.53	3.63
e	1.27BSC		
H	0.60	0.70	0.80
L	0.50	0.70	0.80
θ	-	-	10°