

N-Channel and P-Channel 40V(D-S) MOSFET

Product summary			
V _{DS}	40	-40	V
R _{DS(ON)} (at V _{GS} =10V) Typ.	9.9	19.2	mΩ
R _{DS(ON)} (at V _{GS} =4.5V) Typ.	13.5	24.2	mΩ
I _D (T _C =25°C)	34	-24	A

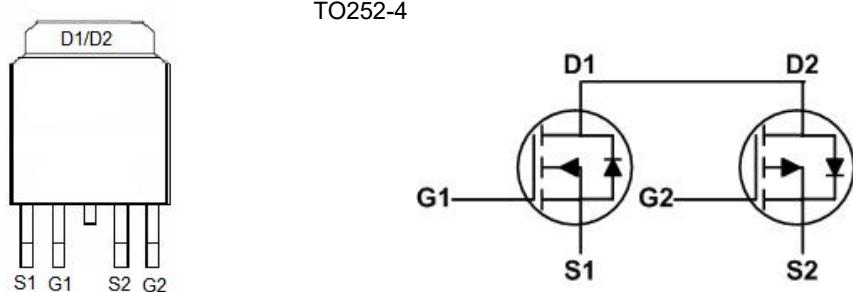
Features

- Low Gate Charge
- Advanced Trench Technology

Applications

- Power management functions
- PWM Application
- Load switch

Pin Configuration



Packing Information

Device	Package	Reel Size	Quantity(Min. Package)
ECFD30C04	TO252-4	13"	2500pcs

Absolute Maximum Ratings (at T_A=25°C Unless Otherwise Noted)

Symbol	Parameter	N-Rating	P-Rating	Units
V _{DS}	Drain-Source Voltage	40	-40	V
V _{GS}	Gate-Source Voltage	±20	±20	V
I _D	Continuous Drain Current	T _C =25°C	34	-24
		T _C =100°C	21.5	-15.2
I _{DM}	Pulse Drain Current Tested ^A	134	-95	A
E _{AS}	Single Pulse Avalanche Energy ^B	33	42	mJ
P _D	Power Dissipation T _C =25°C	25	25	W
T _J , T _{STG}	Junction and Storage Temperature Range	-55 to +150	-55 to +150	°C

Thermal Characteristics

Symbol	Parameter	Typical	Units
R _{θJC}	Thermal Resistance Junction-Case	5	°C/W

N-Channel Electrical Characteristics (at $T_J = 25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Condition	Min.	Typ.	Max.	Units
Static Parameters						
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_{\text{D}}=250\mu\text{A}$	40	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$\text{V}_{\text{DS}}=40\text{V}, \text{V}_{\text{GS}}=0\text{V}$	--	--	1	μA
I_{GSS}	Gate-Body Leakage Current	$\text{V}_{\text{DS}}=0\text{V}, \text{V}_{\text{GS}}=\pm 20\text{V}$	--	--	± 100	nA
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_{\text{D}}=250\mu\text{A}$	1.0	1.5	2.2	V
$\text{R}_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance ^C	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_{\text{D}}=10\text{A}$	--	9.9	12.6	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_{\text{D}}=5\text{A}$	--	13.5	17	$\text{m}\Omega$
V_{SD}	Diode Forward Voltage	$\text{I}_{\text{S}}=10\text{A}, \text{V}_{\text{GS}}=0\text{V}$	--	--	1.2	V
I_{S}	Maximum Body-Diode Continuous Current		--	--	34	A
Dynamic Parameters ^D						
C_{iss}	Input Capacitance	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=20\text{V}$ $f=1\text{MHz}$	--	1210	--	pF
C_{oss}	Output Capacitance		--	85	--	pF
C_{rss}	Reverse Transfer Capacitance		--	56	--	pF
Q_{g}	Total Gate Charge	$\text{V}_{\text{DS}}=20\text{V}, \text{I}_{\text{D}}=10\text{A}$ $\text{V}_{\text{GS}}=0 \text{ to } 10\text{V}$	--	27	--	nC
Q_{gs}	Gate-Source Charge		--	8	--	nC
Q_{gd}	Gate-Drain Charge		--	6	--	nC
$\text{t}_{\text{D}(\text{on})}$	Turn-on Delay Time	$\text{V}_{\text{DD}}=20\text{V}$ $\text{I}_{\text{D}}=10\text{A}, \text{R}_{\text{GEN}}=3\Omega$ $\text{V}_{\text{GS}}=10\text{V}$	--	7.8	--	ns
t_{r}	Turn-on Rise Time		--	12.3	--	ns
$\text{t}_{\text{D}(\text{off})}$	Turn-off Delay Time		--	27.5	--	ns
t_{f}	Turn-off Fall Time		--	6.9	--	ns
t_{rr}	Reverse Recovery Time	$\text{I}_{\text{F}}=10\text{A}$ $d\text{I}/dt=100\text{A}/\mu\text{s}$	--	11	--	ns
Q_{rr}	Reverse Recovery Charge		--	7	--	nC

A. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

B. The E_{AS} data shows Max. rating . The test condition is $T_J=25^\circ\text{C}, \text{V}_{\text{DD}}=20\text{V}, \text{R}_G=25\text{ohm}, \text{V}_{\text{G}}=10\text{V}, \text{L}=0.5\text{mH}, \text{I}_{\text{AS}}=11.5\text{A}$.

C. Pulse Test: Pulse Width $\leq 300\text{us}$, Duty cycle $\leq 0.5\%$.

D. Guaranteed by design, not subject to production testing.

P-Channel Electrical Characteristics (at $T_J = 25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Condition	Min.	Typ.	Max.	Units
Static Parameters						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-40	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=-40\text{V}, V_{\text{GS}}=0\text{V}$	--	--	-1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 20\text{V}$	--	--	± 100	nA
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1.0	-1.7	-2.5	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance ^C	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-5\text{A}$	--	19.2	25	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-4\text{A}$	--	24.2	31.5	$\text{m}\Omega$
V_{SD}	Diode Forward Voltage	$I_{\text{S}}=-1\text{A}, V_{\text{GS}}=0\text{V}$	--	--	-1.0	V
I_{S}	Maximum Body-Diode Continuous Current		--	--	-24	A
Dynamic Parameters ^D						
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=-20\text{V}$ $f=1\text{MHz}$	--	1436	--	pF
C_{oss}	Output Capacitance		--	122	--	pF
C_{rss}	Reverse Transfer Capacitance		--	85	--	pF
Q_{g}	Total Gate Charge	$V_{\text{DS}}=-20\text{V}, I_{\text{D}}=-10\text{A}$ $V_{\text{GS}}=0 \text{ to } -10\text{V}$	--	28	--	nC
Q_{gs}	Gate-Source Charge		--	7	--	nC
Q_{gd}	Gate-Drain Charge		--	5.5	--	nC
$t_{\text{D}(\text{on})}$	Turn-on Delay Time	$V_{\text{DD}}=-20\text{V}$ $I_{\text{D}}=-10\text{A}, R_{\text{GEN}}=3\Omega$ $V_{\text{GS}}=-10\text{V}$	--	7.8	--	ns
t_{r}	Turn-on Rise Time		--	29	--	ns
$t_{\text{D}(\text{off})}$	Turn-off Delay Time		--	36	--	ns
t_{f}	Turn-off Fall Time		--	47	--	ns
t_{rr}	Reverse Recovery Time	$I_{\text{F}}=-10\text{A}$ $di/dt=100\text{A}/\mu\text{s}$	--	14	--	ns
Q_{rr}	Reverse Recovery Charge		--	7	--	nC

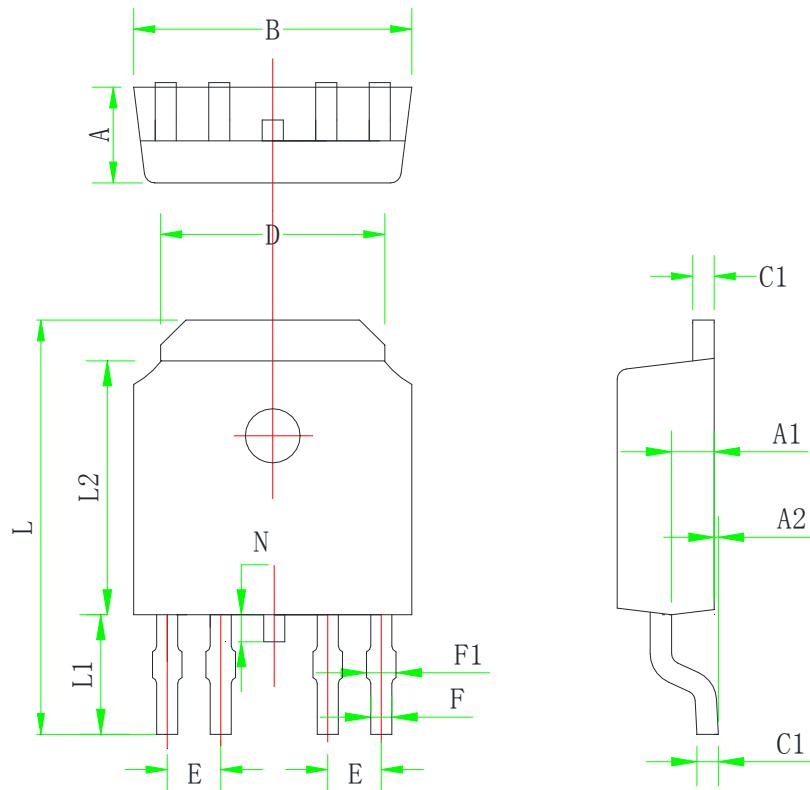
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C. Pulse Test: Pulse Width $\leq 300\text{us}$, Duty cycle $\leq 0.5\%$.

D. Guaranteed by design, not subject to production testing.

TO252-4L Package Information



Symbol	Min	Typ	Max
A	2.20	2.30	2.40
A1	0.91	1.01	1.11
A2	0.05	0.15	0.25
B	6.45	6.60	6.75
C	0.45	0.50	0.58
C1	0.45	0.50	0.58
D	5.12	5.32	5.52
E		1.27 TYP	
F	0.45	0.60	0.75
F1	0.40	0.50	0.60
L	9.70	10.00	10.20
L1	2.6	2.8	3.0
L2	5.95	6.10	6.25
N	0.45	0.65	0.85