

N-Channel 100V(D-S) MOSFET

Product summary		
V_{DS}	100	V
$R_{DS(ON)}$ (at $V_{GS}=10V$) Typ.	4.4	m Ω
I_D ($T_C=25^{\circ}C$)	128	A

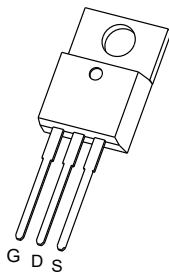
Features

- Low Gate Charge
- High Current Capability

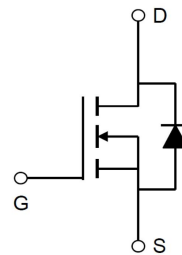
Applications

- Current Switching
- Motor Driving
- Power management

Pin Configuration



TO-220



Packing Information

Device	Package	Packaging	Quantity
ECFB128N10A	TO-220	Tube	50pcs

Absolute Maximum Ratings (at $T_A=25^{\circ}C$ Unless Otherwise Noted)

Symbol	Parameter		Rating	Units
V_{DS}	Drain-Source Voltage		100	V
V_{GS}	Gate-Source Voltage		± 20	V
I_D	Continuous Drain Current ^A	$T_C=25^{\circ}C$	128	A
		$T_C=100^{\circ}C$	81	A
I_{DM}	Pulse Drain Current Tested ^B		417	A
I_{AS}	Avalanche Current ^C		42	A
E_{AS}	Single Pulse Avalanche Energy ^C		265	mJ
P_D	Power Dissipation ^D		167	W
T_J, T_{STG}	Junction and Storage Temperature Range		-55 to +150	$^{\circ}C$

Thermal Characteristics

Symbol	Parameter	Typical	Units
$R_{\theta JC}$	Thermal Resistance-Junction to case	0.75	$^{\circ}C/W$

Electrical Characteristics (at T_J =25°C Unless Otherwise Noted)

Symbol	Parameter	Condition	Min.	Typ.	Max.	Units
Static Parameters						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	100	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =80V, V _{GS} =0V	--	--	1	uA
I _{GSS}	Gate-Body Leakage Current	V _{DS} =0V, V _{GS} =±20V	--	--	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	2.0	2.7	4.0	V
R _{DS(ON)}	Drain-Source On-State Resistance ^E	V _{GS} =10V, I _D =20A	--	4.4	5.3	mΩ
V _{SD}	Diode Forward Voltage	I _S =1A, V _{GS} =0V	--	--	1.0	V
I _S	Diode Continuous Current	T _C =25°C	--	--	167	A
Dynamic Parameters ^F						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =50V f=1MHZ	--	2816	--	pF
C _{oss}	Output Capacitance		--	614	--	pF
C _{rss}	Reverse Transfer Capacitance		--	7.4	--	pF
R _g	Gate Resistance	V _{GS} =0V, V _{DS} =0V f=1MHZ	--	2.4	--	Ω
Q _g	Total Gate Charge	V _{DS} =50V, I _D =20A V _{GS} =10V	--	42	--	nC
Q _{gs}	Gate-Source Charge		--	9.7	--	nC
Q _{gd}	Gate-Drain Charge		--	10.6	--	nC
t _{D(on)}	Turn-on Delay Time	V _{DS} =50V, R _L =2.5Ω, R _{GEN} =6Ω, V _{GS} =10V	--	13	--	ns
t _r	Turn-on Rise Time		--	25	--	ns
t _{D(off)}	Turn-off Delay Time		--	43	--	ns
t _f	Turn-off Fall Time		--	37	--	ns
t _{rr}	Reverse recovery time	I _F =15A, di/dt=100 A/uS	--	60	--	ns
Q _{rr}	Reverse recovery charge		--	61	--	nC

A. Computed continuous current assumes the condition of T_J_Max while the actual continuous current depends on the thermal & electro-mechanical application board design.

B. This single-pulse measurement was taken under T_J_Max = 150°C.

C. This single-pulse measurement was taken under the following condition [L = 300uH, V_{GS}=10V, V_{DS}=50V] while its value is limited by T_J_Max = 150°C.

D. The power dissipation PD is based on T_J_Max = 150°C.

E. The data tested by pulsed , pulse width≤300us , duty cycle≤0.5%.

F. Guaranteed by design, not subject to production testing.

Typical Characteristics

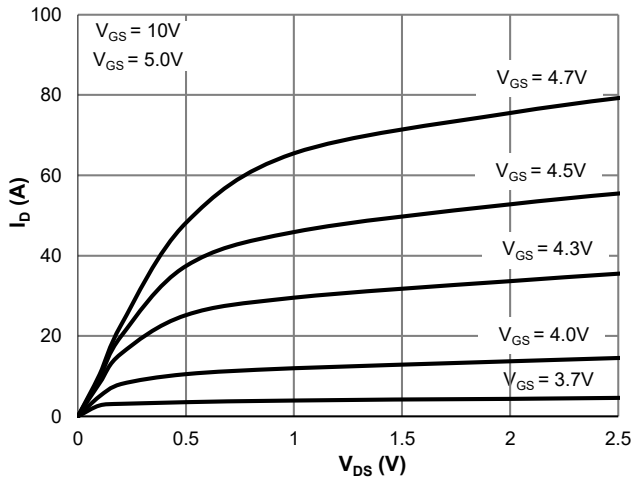


Figure 1: Saturation Characteristics

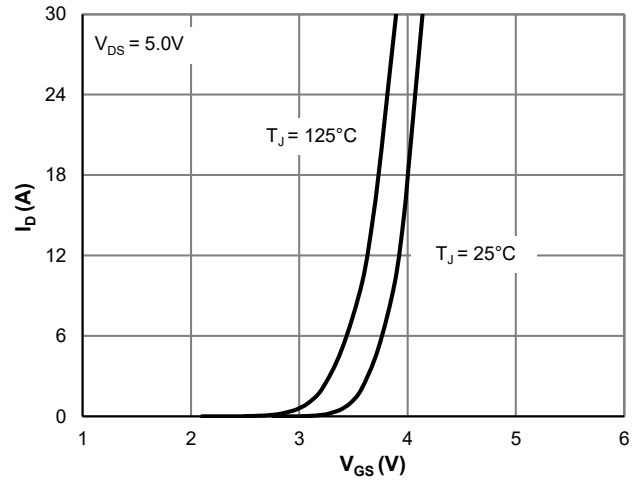


Figure 2: Transfer Characteristics

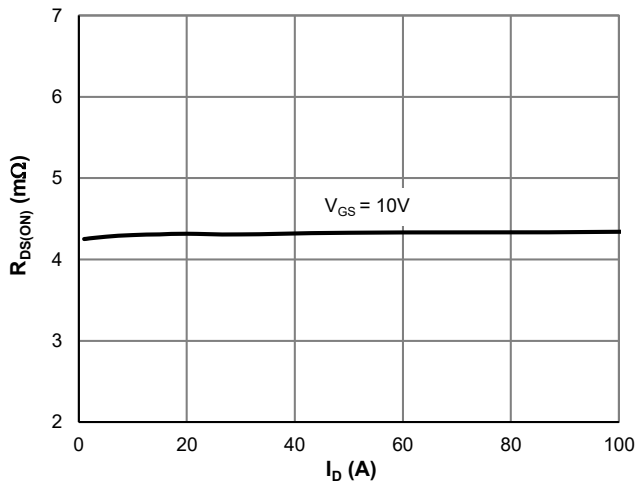


Figure 3: $R_{DS(ON)}$ vs. Drain Current

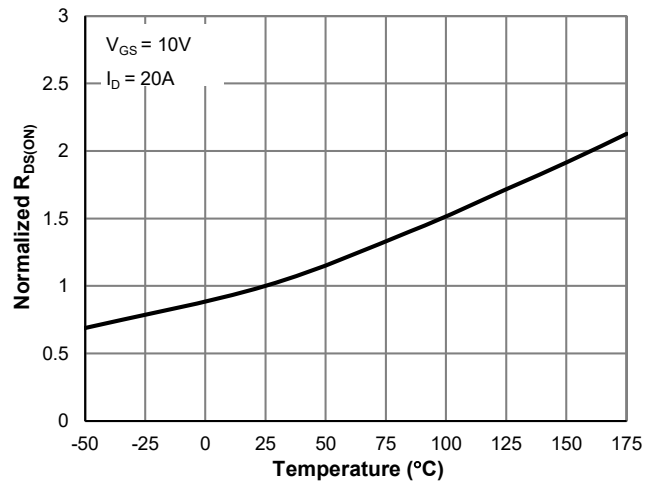


Figure 4: $R_{DS(ON)}$ vs. Junction Temperature

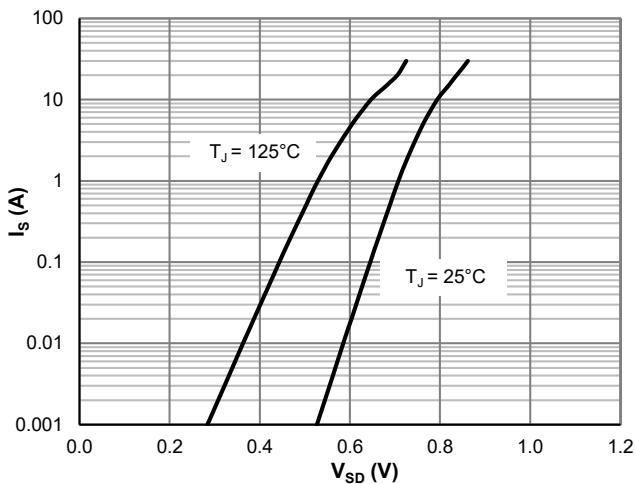


Figure 5: Body-Diode Characteristics

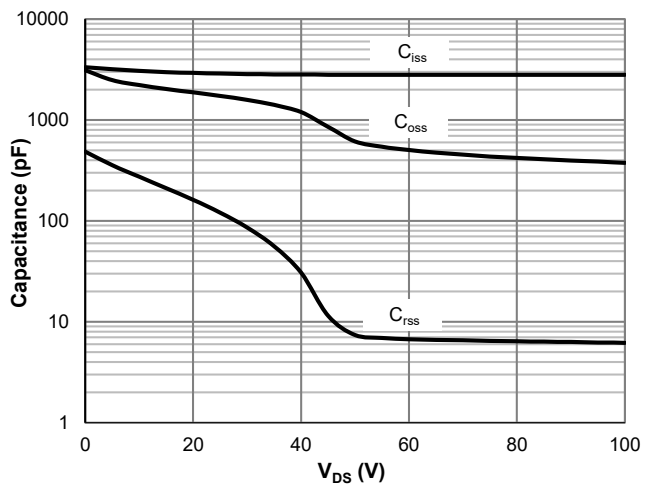


Figure 6: Capacitance Characteristics

Typical Characteristics

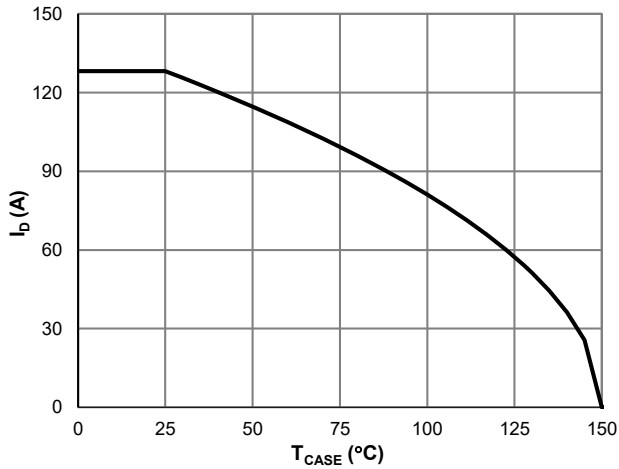


Figure 7: Current De-rating

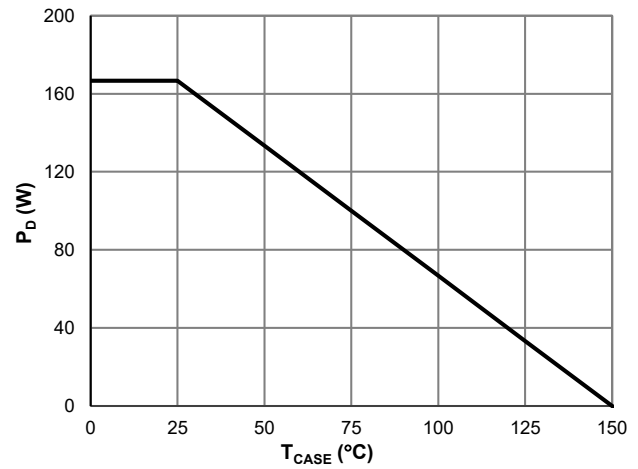


Figure 8: Power De-rating

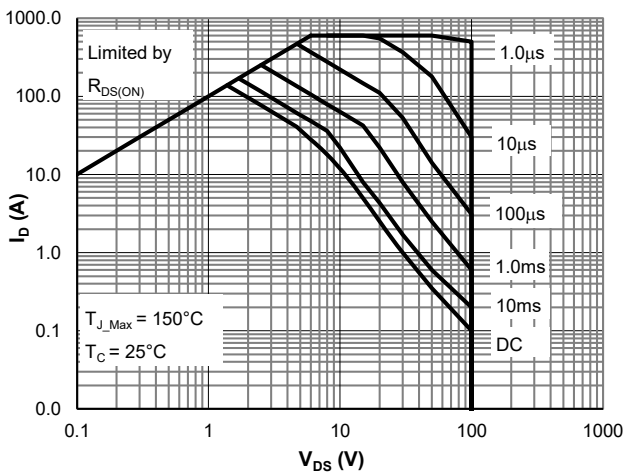


Figure 9: Maximum Safe Operating Area

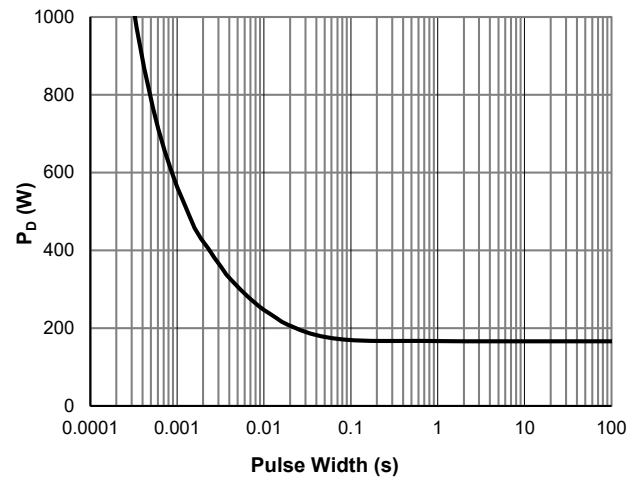


Figure 10: Single Pulse Power Rating, Junction-to-Case

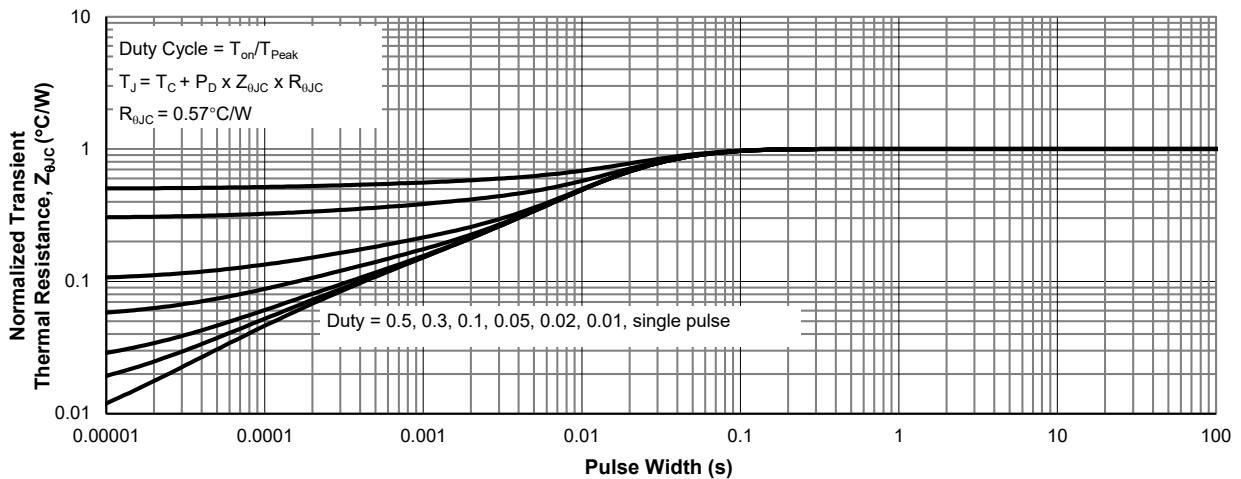
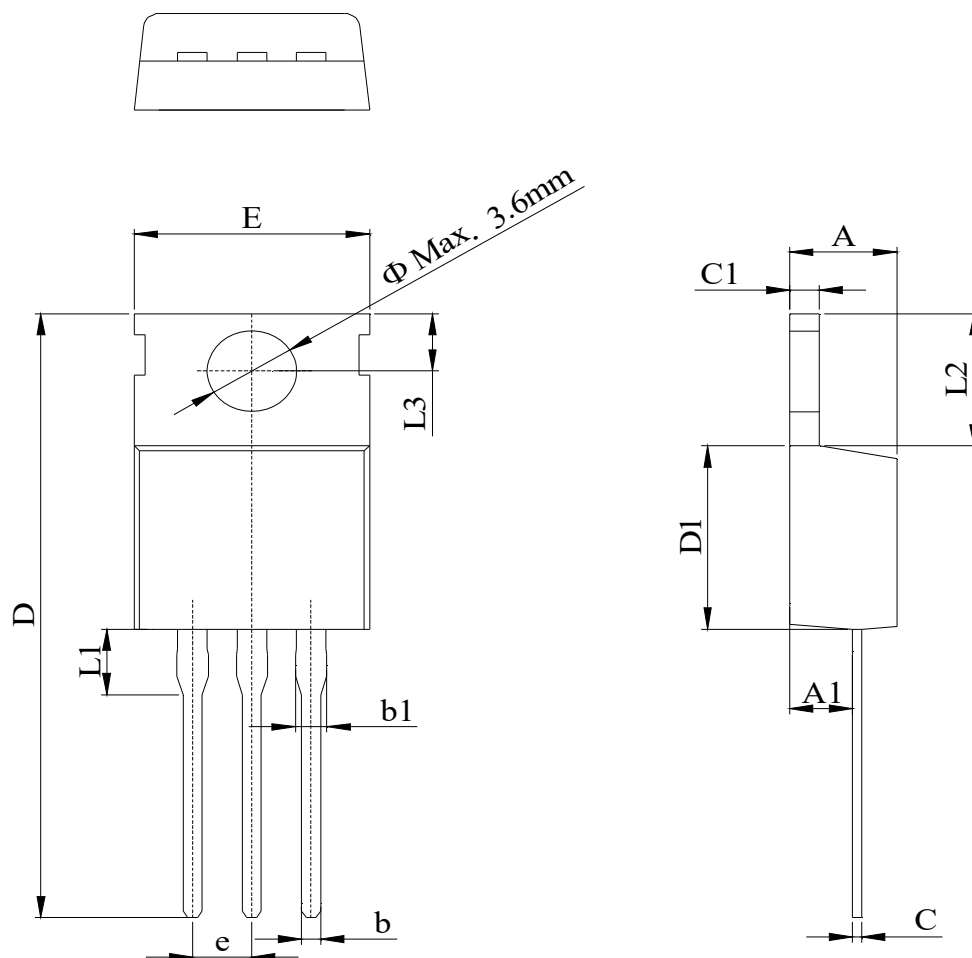


Figure 11: Normalized Maximum Transient Thermal Impedance

TO-220 Package Information



DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	4.37		4.70
A1	2.20		3.00
b	0.70		0.95
b1	1.14		1.70
C	0.45		0.60
C1	1.23		1.40
D	28.00		29.80
D1	8.80		9.90
E	9.70		10.50
L1			3.80
L2	6.25		6.90
L3	2.40		3.00
e		2.54 BSC	