

N-Channel 20V(D-S) MOSFET

Product summary		
V_{DS}	20	V
$R_{DS(on)}$ (at $V_{GS}=4.5V$) Typ.	170	$m\Omega$
$R_{DS(on)}$ (at $V_{GS}=2.5V$) Typ.	240	$m\Omega$
$I_D(T_A=25^\circ C)$	0.5	A

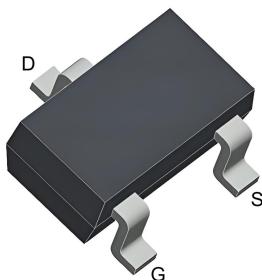
Features

- Operated at Low Logic Level Gate Drive
- Low $R_{DS(on)}$
- ESD protection up to 2 kV

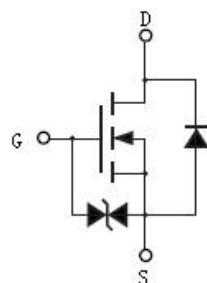
Applications

- Load Switching
- Logic Level Shift

Pin Configuration



SOT-23



Packing Information

Device	Package	Reel Size	Quantity(Min. Package)
ECDA1012E	SOT-23	7"	3000pcs

Absolute Maximum Ratings (at $T_A=25^\circ C$ Unless Otherwise Noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 10	V
I_D	Continuous Drain Current at $V_{GS}=10V$ ^A	$T_A=25^\circ C$	A
		$T_A=70^\circ C$	A
I_{DM}	Pulse Drain Current Tested ^B	2.3	A
P_D	Power Dissipation ^A	$T_A=25^\circ C$	W
T_J, T_{STG}	Junction and Storage Temperature Range	-55 to +150	°C

Thermal Characteristics

Symbol	Parameter	Typical	Units
$R_{\theta JA}$	Thermal Resistance-Junction to ambient ^A	390	°C/W

Electrical Characteristics (at $T_J = 25^\circ\text{C}$ Unless Otherwise Noted)

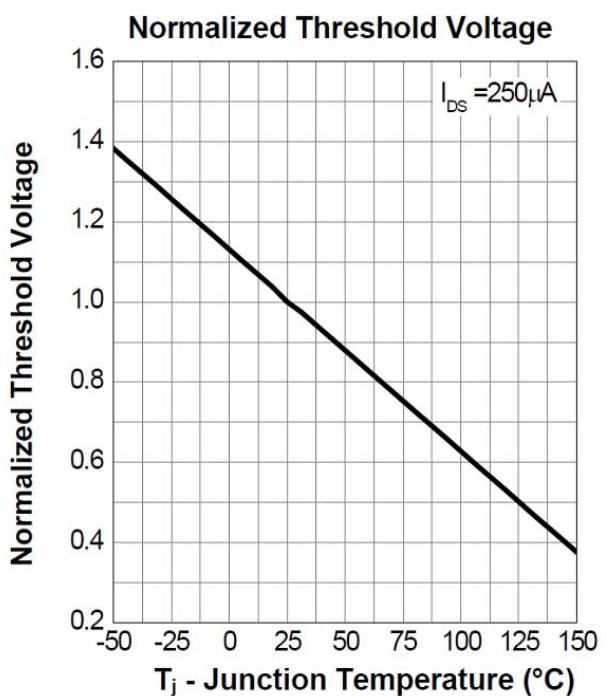
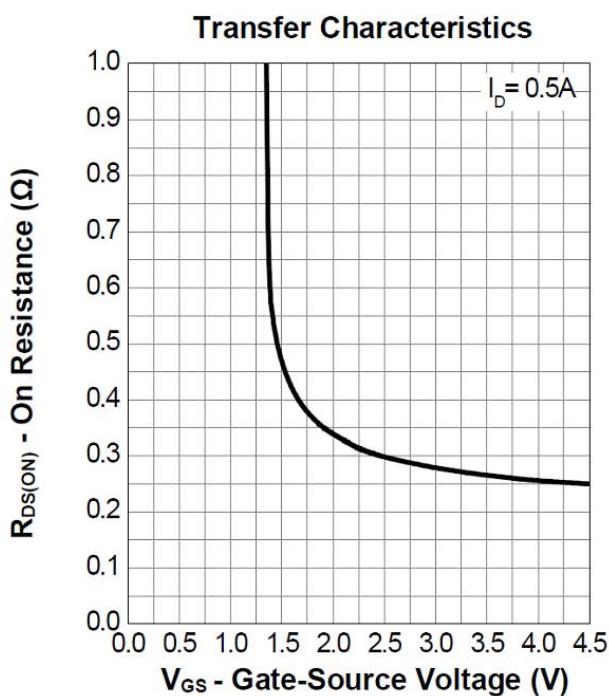
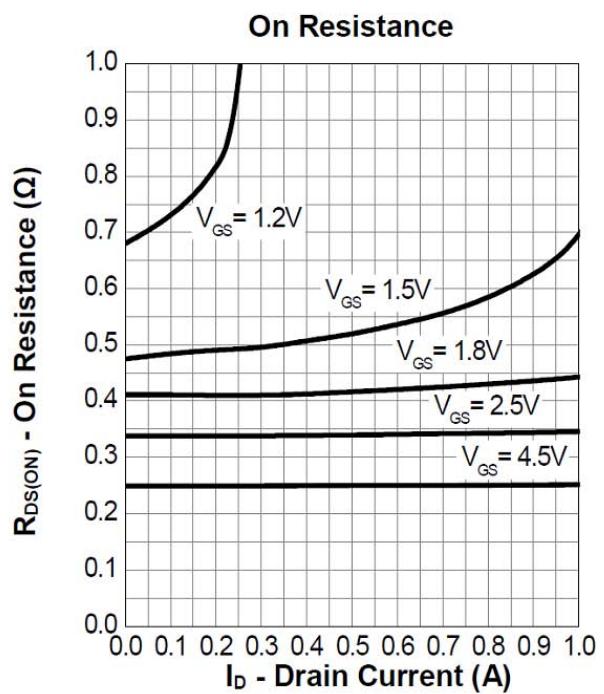
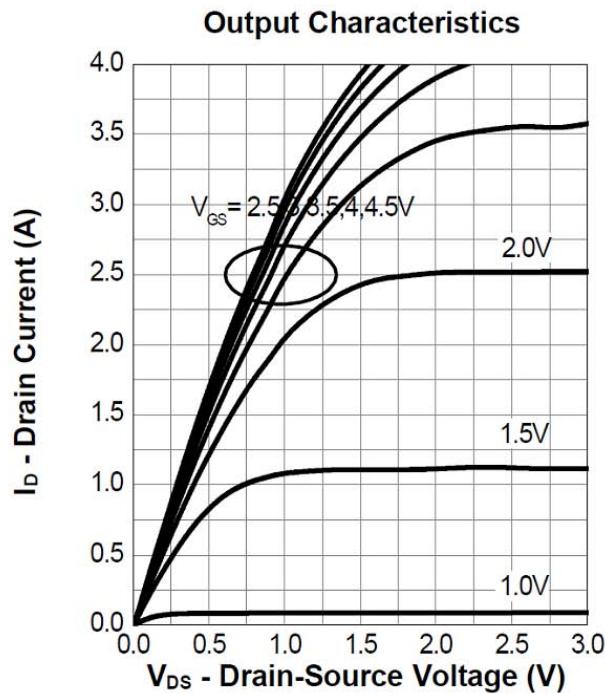
Symbol	Parameter	Condition	Min.	Typ.	Max.	Units
Static Parameters						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	20	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 10\text{V}$	--	± 2	± 10	μA
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	0.35	0.75	1.1	V
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance ^B	$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=0.5\text{A}$	--	170	220	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}, I_{\text{D}}=0.4\text{A}$	--	240	350	$\text{m}\Omega$
V_{SD}	Forward Voltage	$I_{\text{SD}}=0.5\text{A}, V_{\text{GS}}=0\text{V}$	--	--	1.2	V
Dynamic Parameters ^C						
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=10\text{V}$ $f=1\text{MHz}$	--	67	--	pF
C_{oss}	Output Capacitance		--	18	--	pF
C_{rss}	Reverse Transfer Capacitance		--	6	--	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=0.5\text{A}$ $V_{\text{GS}}=4.5\text{V}$	--	1.4	--	nC
Q_{gs}	Gate-Source Charge		--	0.21	--	nC
Q_{gd}	Gate-Drain Charge		--	0.21	--	nC
$t_{\text{D(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=10\text{V}$ $I_{\text{D}}=0.15\text{A}$, $R_{\text{GEN}}=10\Omega$, $V_{\text{GS}}=4.5\text{V}$	--	2.8	--	nS
t_r	Turn-on Rise Time		--	20	--	nS
$t_{\text{D(off)}}$	Turn-off Delay Time		--	23	--	nS
t_f	Turn-off Fall Time		--	24	--	nS

A. The data tested by surface mounted on a 1 inch x 1 inch FR-4 board with 2OZ copper.

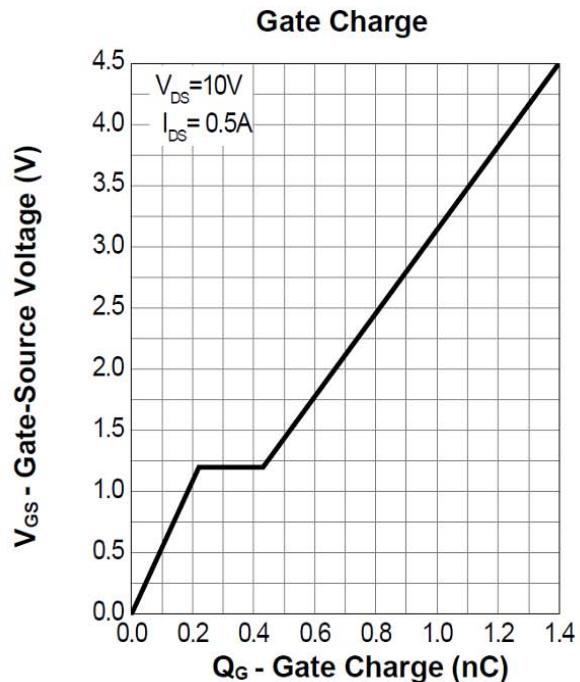
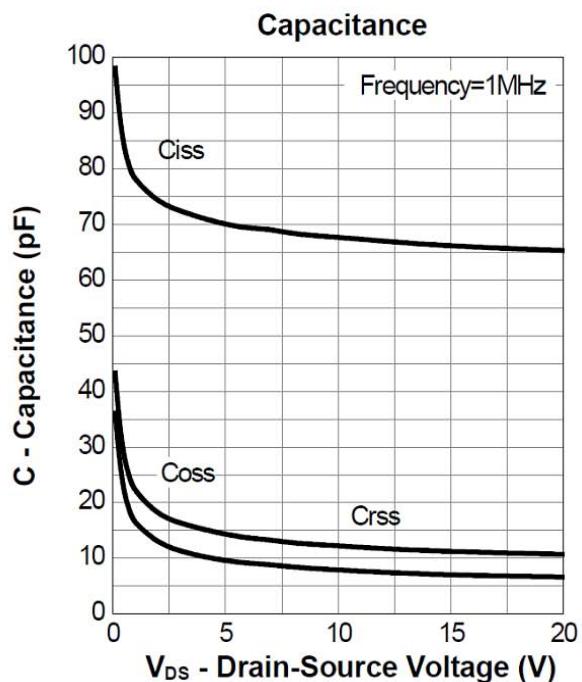
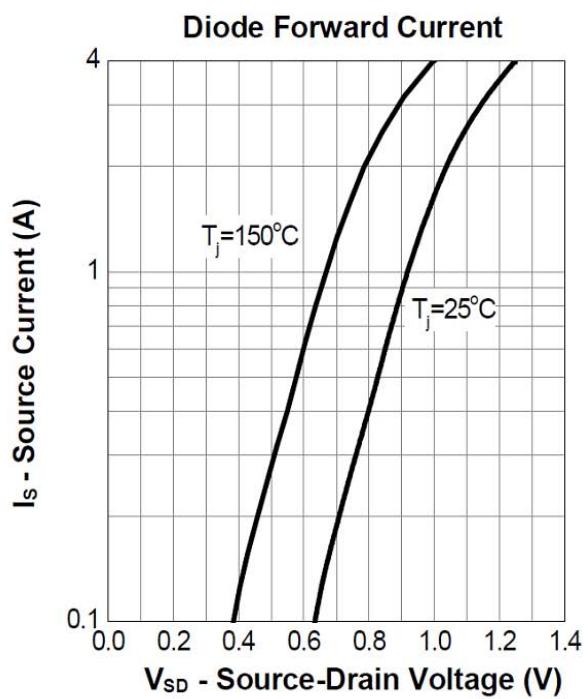
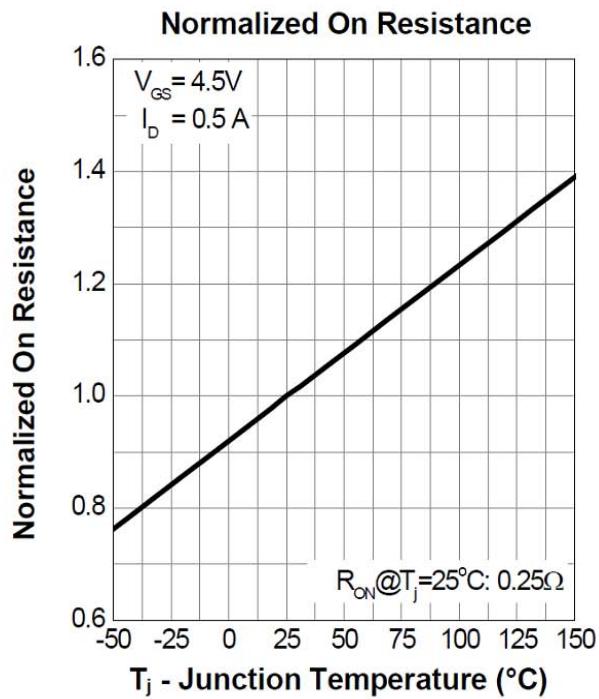
B. Pulse Test: Pulse Width $\leq 300\text{us}$, Duty cycle $\leq 2\%$.

C. Guaranteed by design, not subject to production testing.

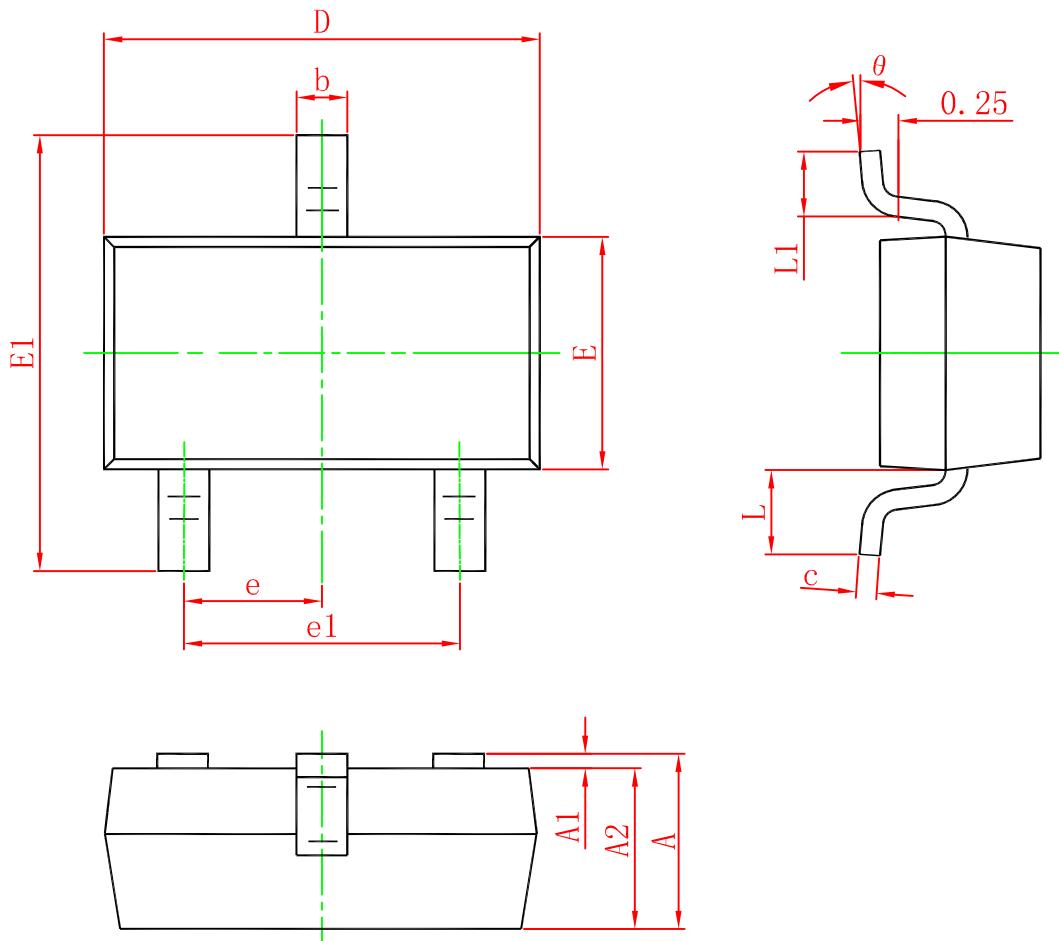
Typical Characteristics



Typical Characteristics



SOT-23 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°