

N-Channel 150V(D-S) MOSFET

Product summary		
V_{DS}	150	V
$R_{DS(ON)}$ (at $V_{GS}=10V$) Typ.	5.7	$m\Omega$
$I_D(T_c=25^\circ C)$	115	A

Features
<ul style="list-style-type: none"> Split gate trench MOSFET technology High density cell design for low $R_{DS(ON)}$
Applications
<ul style="list-style-type: none"> PWM Application Load switching DC-DC convertor

Pin Configuration



Packing Information

Device	Package	Reel Size	Quantity(Min. Package)
ECFC115N15G	TO-263	13"	800pcs

Absolute Maximum Ratings (at $T_A=25^\circ C$ Unless Otherwise Noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	150	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current	$T_c=25^\circ C$	A
		$T_c=100^\circ C$	A
I_{DM}	Pulse Drain Current Tested ^A	460	A
E_{AS}	Single Pulse Avalanche Energy ^B	361	mJ
P_D	Power Dissipation $T_c=25^\circ C$	220	W
T_J, T_{STG}	Junction and Storage Temperature Range	-55 to +150	°C

Thermal Characteristics

Symbol	Parameter	Typical	Units
R_{eJC}	Thermal Resistance-Junction to case	0.57	°C/W
R_{eJA}	Thermal Resistance-Junction to ambient	62	°C/W

Electrical Characteristics (at $T_J = 25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Condition	Min.	Typ.	Max.	Units
Static Parameters						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	150	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=150\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 20\text{V}$	--	--	± 100	nA
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	2.4	3.0	3.6	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance ^B	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=40\text{A}$	--	5.7	6.8	$\text{m}\Omega$
Dynamic Parameters ^D						
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=75\text{V}$ $f=1\text{MHz}$	--	6200	--	pF
C_{oss}	Output Capacitance		--	470	--	pF
C_{rss}	Reverse Transfer Capacitance		--	9	--	pF
R_g	Gate Resistance	$f = 1\text{MHz}$	--	0.6	--	Ω
Q_g	Total Gate Charge	$V_{\text{DS}}=75\text{V}, I_{\text{D}}=50\text{A}$ $V_{\text{GS}}=10\text{V}$	--	75	--	nC
Q_{gs}	Gate-Source Charge		--	25	--	nC
Q_{gd}	Gate-Drain Charge		--	12	--	nC
$t_{\text{D}(\text{on})}$	Turn-on Delay Time	$V_{\text{DD}}=75\text{V}, V_{\text{GS}}=10\text{V},$ $I_{\text{D}}=50\text{A},$ $R_g=2.7\Omega$	--	4.4	--	ns
t_r	Turn-on Rise Time		--	24.6	--	ns
$t_{\text{D}(\text{off})}$	Turn-off Delay Time		--	38	--	ns
t_f	Turn-off Fall Time		--	9.5	--	ns
Drain-Source Diode Characteristics						
I_s	Maximum Continuous Drain-Source Diode Forward Current	--	--	115	--	A
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current	--	--	460	--	A
V_{SD}	Diode Forward Voltage	$I_s=20\text{A}, V_{\text{GS}}=0\text{V}$	--	--	1.2	V
t_{rr}	Reverse recovery time	$I_F=50\text{A},$ $dI/dt=100 \text{ A/us}$	--	97	--	ns
Q_{rr}	Reverse recovery charge		--	217	--	nC

A. Repetitive Rating : Drain current limited by the package.

B. The EAS data shows Max. rating . The test condition is $T_J=25^\circ\text{C}, V_{\text{DD}}=100\text{V}, V_{\text{GS}}=10\text{V}, L=0.5\text{mH}, I_{\text{AS}}=38\text{A}$.

C. The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 0.5\%$.

D. Guaranteed by design, not subject to production testing.

Typical Characteristics

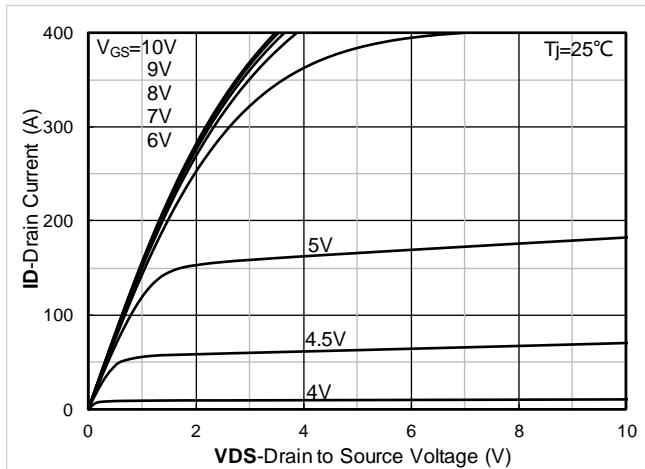


Figure 1. Output Characteristics

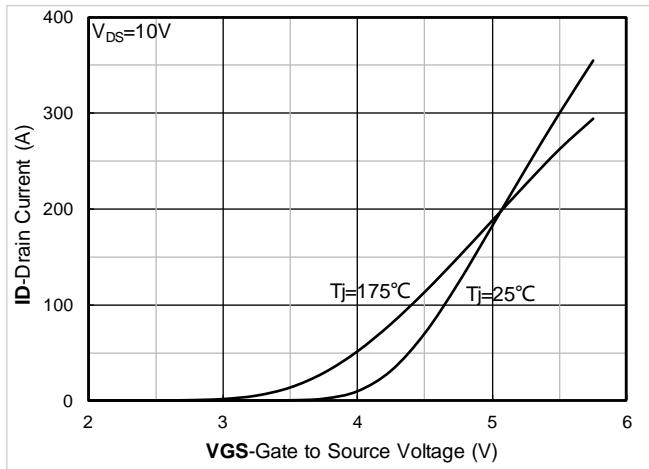


Figure 2. Transfer Characteristics

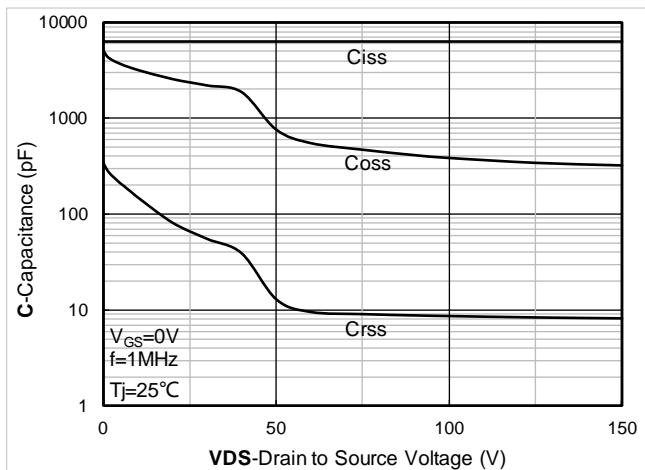


Figure 3. Capacitance Characteristics

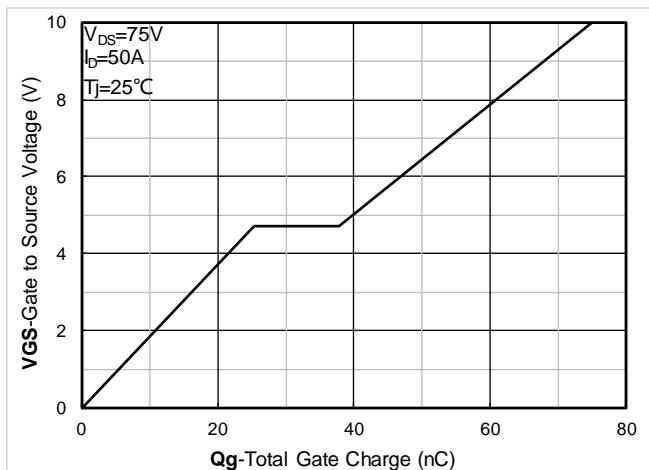


Figure 4. Gate Charge

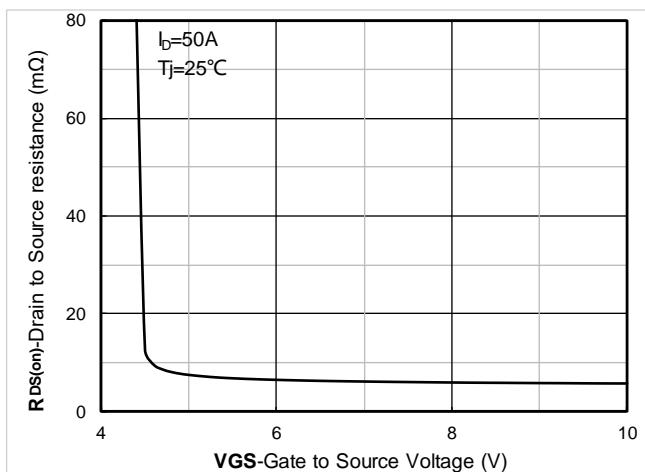


Figure 5. On-Resistance vs Gate to Source Voltage

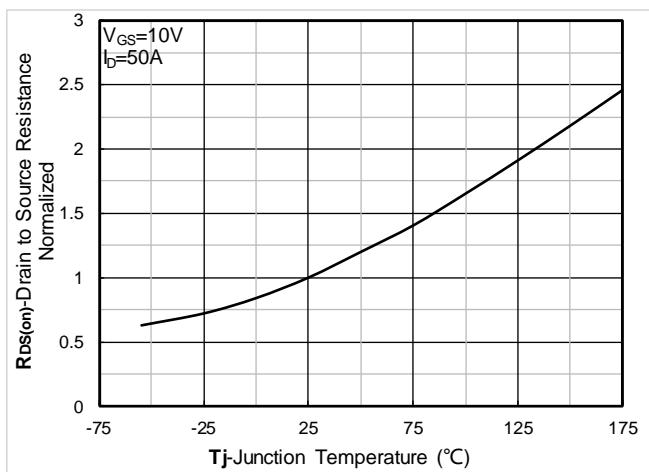
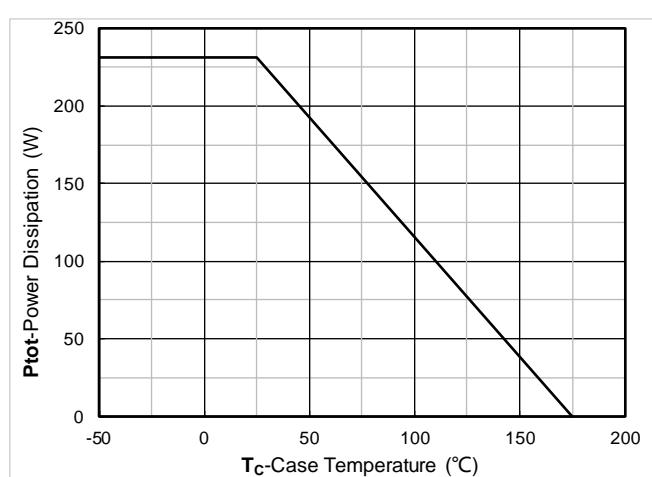
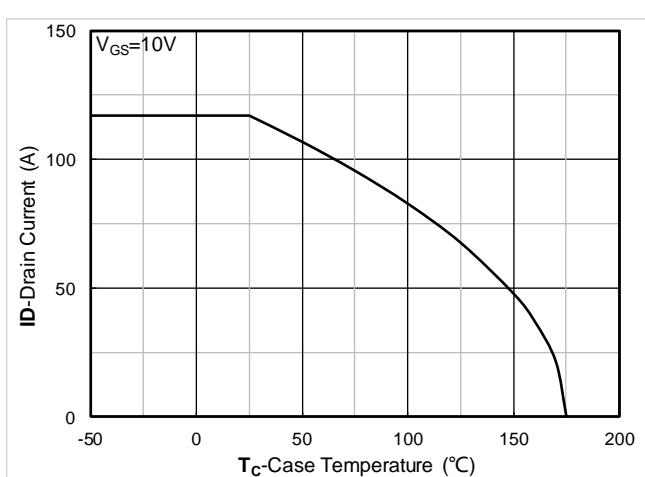
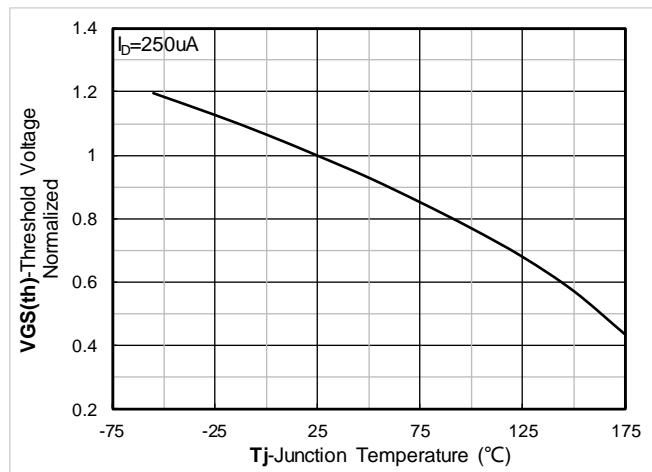
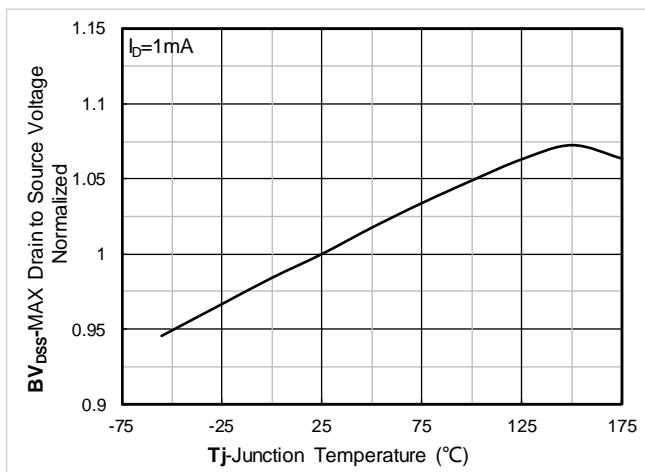
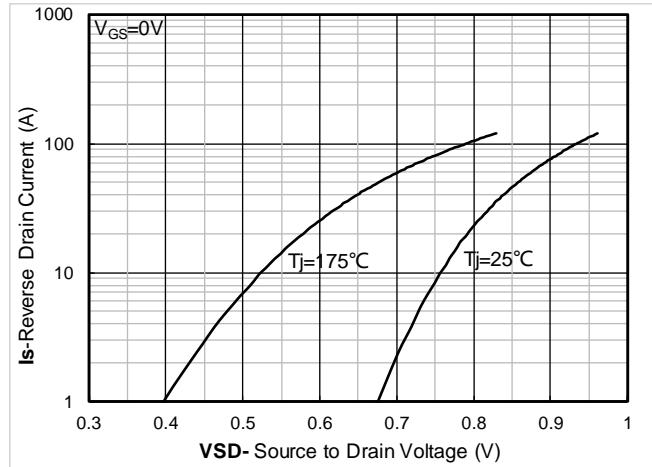
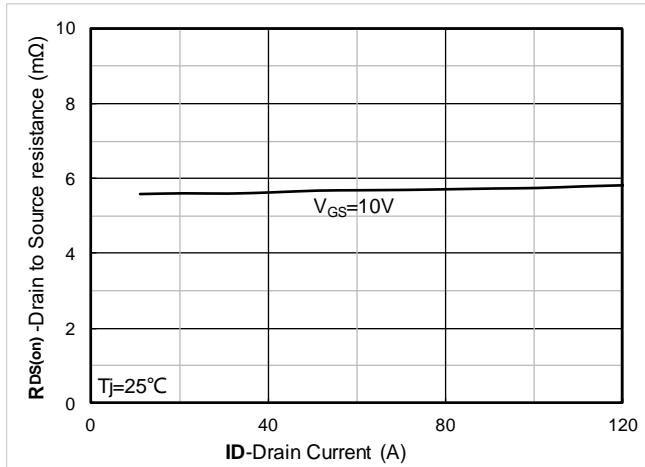


Figure 6. Normalized On-Resistance

Typical Characteristics



Typical Characteristics

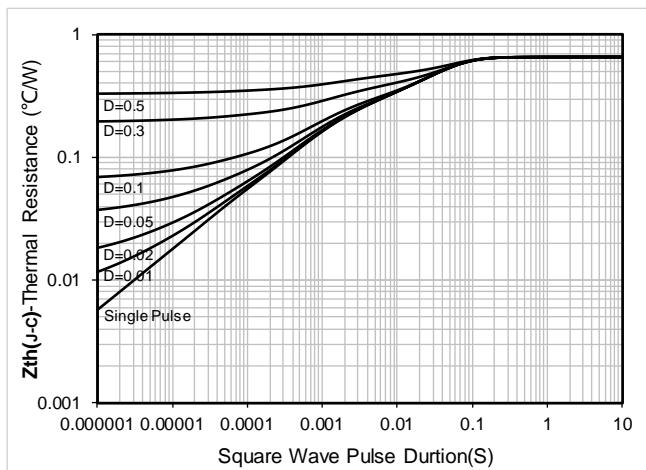


Figure 13. Maximum Transient Thermal Impedance

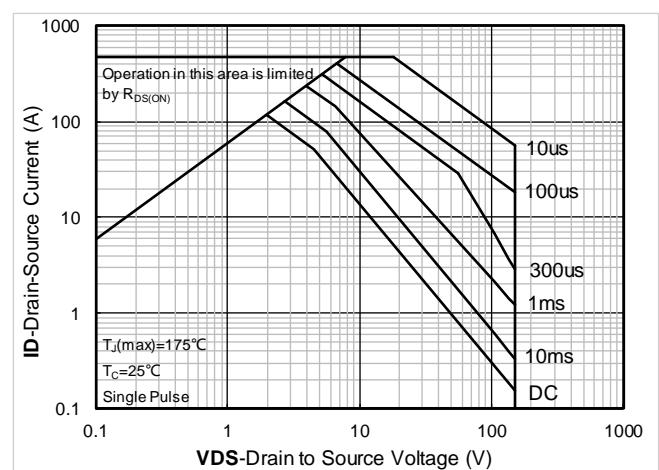
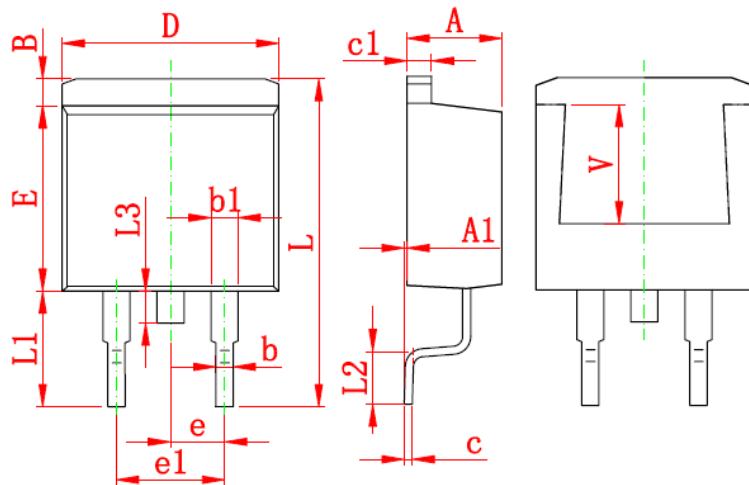


Figure 14. Safe Operation Area

TO-263 Package Information


^{*:Typical}

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	4.470	4.670	0.176	0.184	E	8.500	8.900	0.335	0.350
A1	0.000	0.150	0.000	0.006	e	*2.540		*0.100	
B	1.170	1.370	0.046	0.054	e1	4.980	5.180	0.196	0.204
b	0.710	0.910	0.028	0.036	L	15.050	15.450	0.593	0.608
b1	1.170	1.370	0.046	0.054	L1	5.080	5.480	0.200	0.216
c	0.310	0.530	0.012	0.021	L2	2.340	2.740	0.092	0.108
c1	1.170	1.370	0.046	0.054	L3	1.300	1.700	0.051	0.067
D	10.010	10.310	0.394	0.406	V	5.600	REF	0.220	REF

Notes : 1. Controlling dimension : millimeters.

2. Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.

Material :

- Lead : Pure tin plated.
- Mold Compound : Epoxy resin family, flammability solid burning class:UL94V-0.