

N-Channel 30V(D-S) MOSFET

Product summary		
V _{DS}	30	V
R _{DS(ON)} (at V _{GS} =10V) Typ.	15	mΩ
R _{DS(ON)} (at V _{GS} =4.5V) Typ.	21	mΩ
I _D (T _c =25°C)	12	A

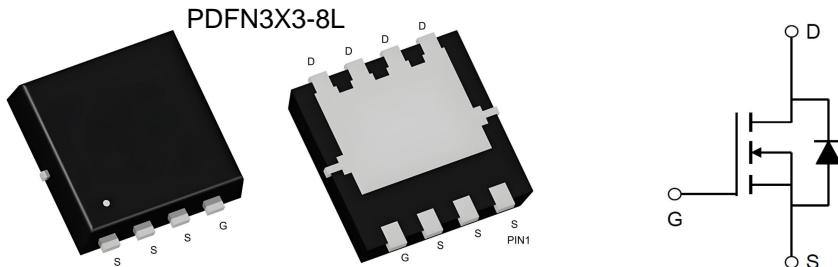
Features

- Low Gate Charge
- Advanced high cell density Trench technology

Applications

- Power management functions

Pin Configuration



Packing Information

Device	Package	Reel Size	Quantity(Min. Package)
ECAL12N03A	PDFN3X3-8L	13"	5000pcs

Absolute Maximum Ratings (at T_A=25°C Unless Otherwise Noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	30	V
V _{GS}	Gate-Source Voltage	±20	V
I _D	Continuous Drain Current	T _C =25°C	A
		T _C =100°C	A
I _{DM}	Pulse Drain Current ^A	48	A
E _{AS}	Single Pulse Avalanche Energy ^B	11	mJ
P _D	Power Dissipation @T _C =25°C	4.8	W
T _J , T _{STG}	Junction and Storage Temperature Range	-55 to +150	°C

Thermal Characteristics

Symbol	Parameter	Typical	Units
R _{θJC}	Thermal Resistance-Junction to case	26	°C/W

Electrical Characteristics (at $T_J = 25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Condition	Min.	Typ.	Max.	Units
Static Parameters						
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	30	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$\text{V}_{\text{DS}}=30\text{V}, \text{V}_{\text{GS}}=0\text{V}$	--	--	1	μA
I_{GSS}	Gate-Body Leakage Current	$\text{V}_{\text{DS}}=0\text{V}, \text{V}_{\text{GS}}=\pm 20\text{V}$	--	--	± 100	nA
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	1.0	1.5	2.5	V
$\text{R}_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance ^C	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=5\text{A}$	--	15	20	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=3\text{A}$	--	21	29	$\text{m}\Omega$
V_{SD}	Diode Forward Voltage	$\text{I}_S=12\text{A}, \text{V}_{\text{GS}}=0\text{V}$	--	--	1.2	V
I_S	Diode Forward Current	$\text{V}_G=\text{V}_D=0\text{V}$	--	--	48	A
Dynamic Parameters ^D						
C_{iss}	Input Capacitance	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=15\text{V}$ $f=1\text{MHz}$	--	490	--	pF
C_{oss}	Output Capacitance		--	79	--	pF
C_{rss}	Reverse Transfer Capacitance		--	61	--	pF
Q_g	Total Gate Charge	$\text{V}_{\text{DS}}=15\text{V}, \text{I}_D=5.8\text{A}$ $\text{V}_{\text{GS}}=10\text{V}$	--	5.2	--	nC
Q_{gs}	Gate-Source Charge		--	0.9	--	nC
Q_{gd}	Gate-Drain Charge		--	1.3	--	nC
$t_{\text{D}(\text{on})}$	Turn-on Delay Time	$\text{V}_{\text{DS}}=15\text{V}$ $\text{I}_D=3\text{A}, \text{R}_{\text{GEN}}=3\Omega$, $\text{V}_{\text{GS}}=10\text{V}$	--	4.5	--	ns
t_r	Turn-on Rise Time		--	2.5	--	ns
$t_{\text{D}(\text{off})}$	Turn-off Delay Time		--	14.5	--	ns
t_f	Turn-off Fall Time		--	3.5	--	ns

A. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

B. The EAS data shows Max. Rating, The test condition is $\text{V}_{\text{DD}}=15\text{V}, \text{V}_{\text{GS}}=10\text{V}, L=0.5\text{mH}, I_{\text{AS}}=6.6\text{A}, R_g=25\Omega, T_J=25^\circ\text{C}$.

C. The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.

D. Guaranteed by design, not subject to production testing.

Typical Characteristics

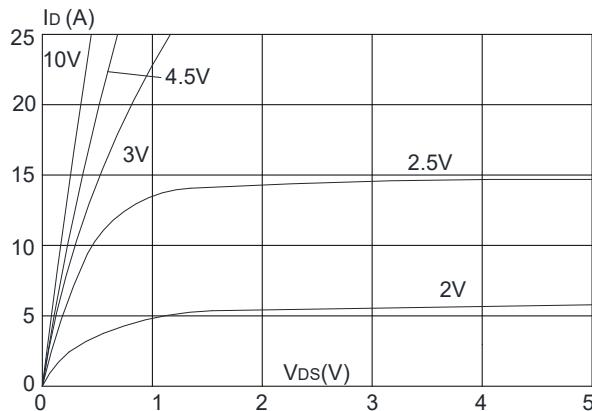


Figure 1: Output Characteristics

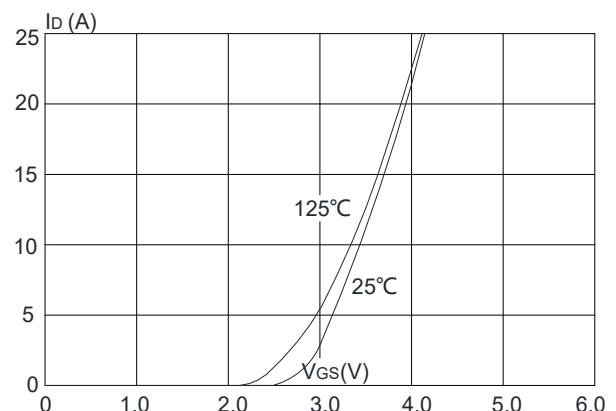


Figure 2: Typical Transfer Characteristics

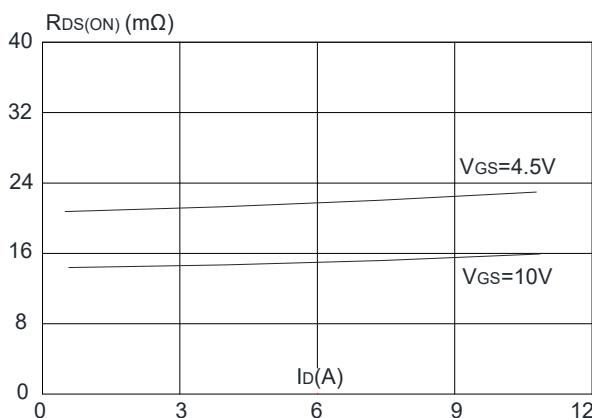


Figure 3: On-resistance vs. Drain Current

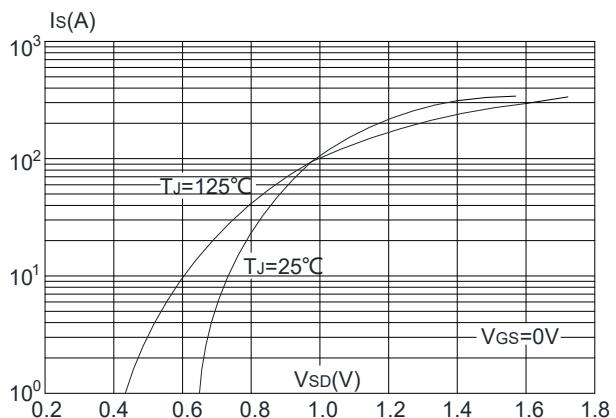


Figure 4: Body Diode Characteristics

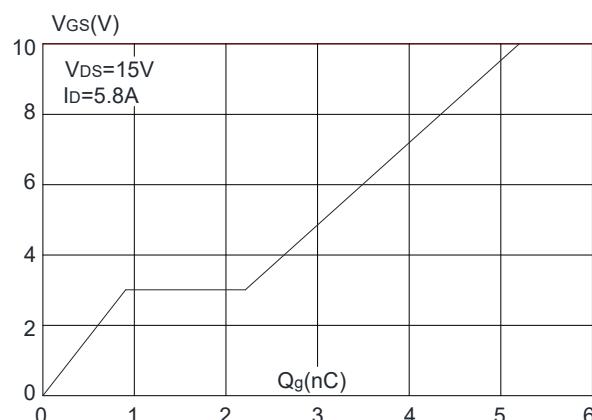


Figure 5: Gate Charge Characteristics

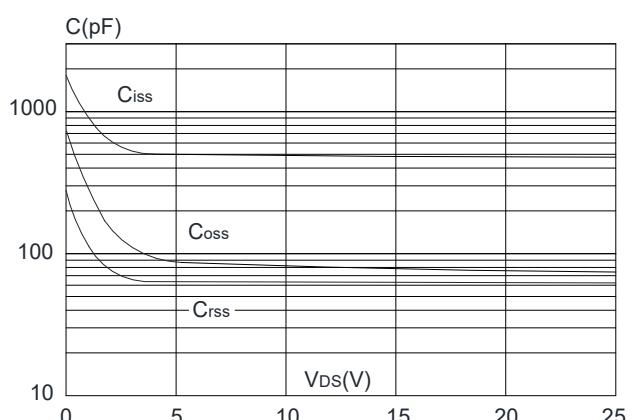


Figure 6: Capacitance Characteristics

Typical Characteristics

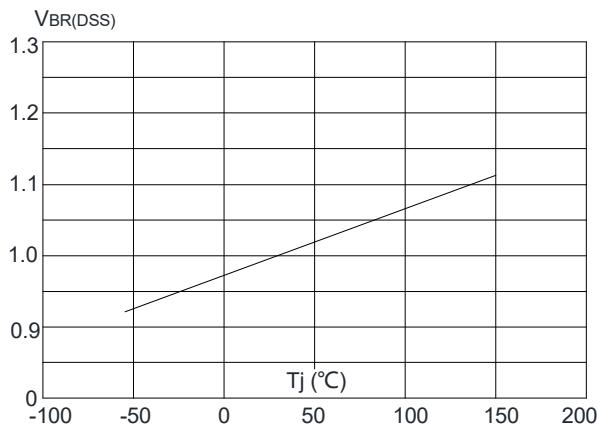


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

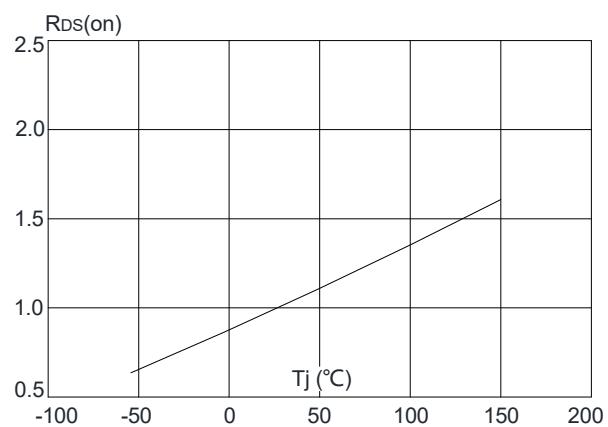


Figure 8: Normalized on Resistance vs. Junction Temperature

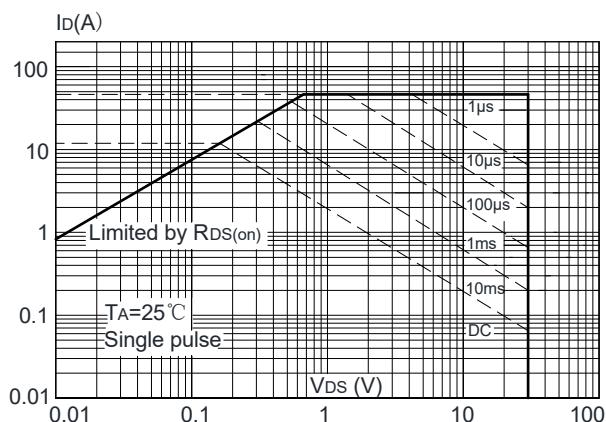


Figure 9: Maximum Safe Operating Area

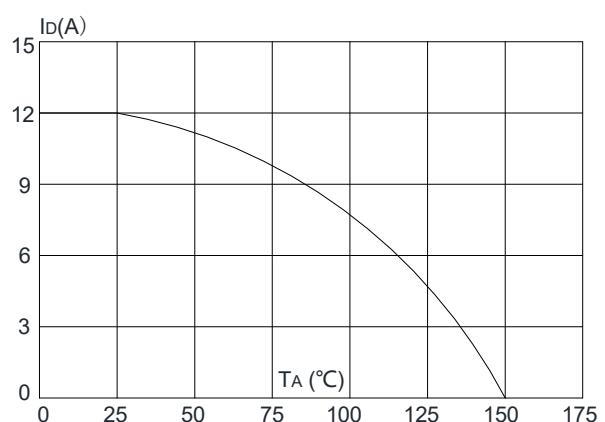


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

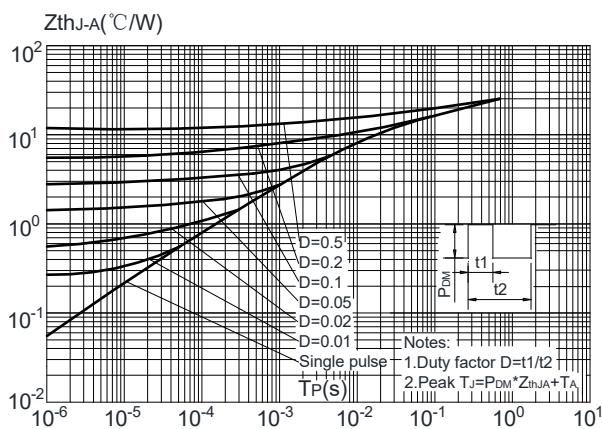


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

Typical Characteristics

Test Circuit

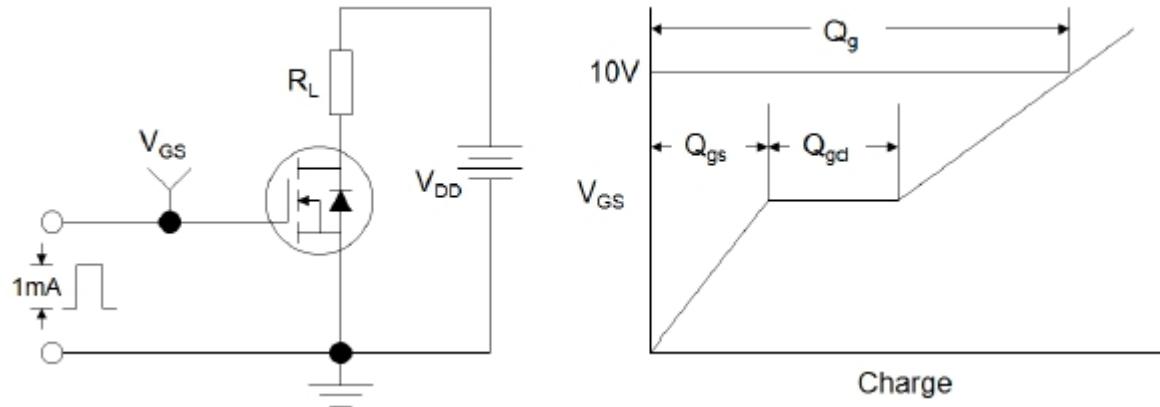


Figure 1: Gate Charge Test Circuit & Waveform

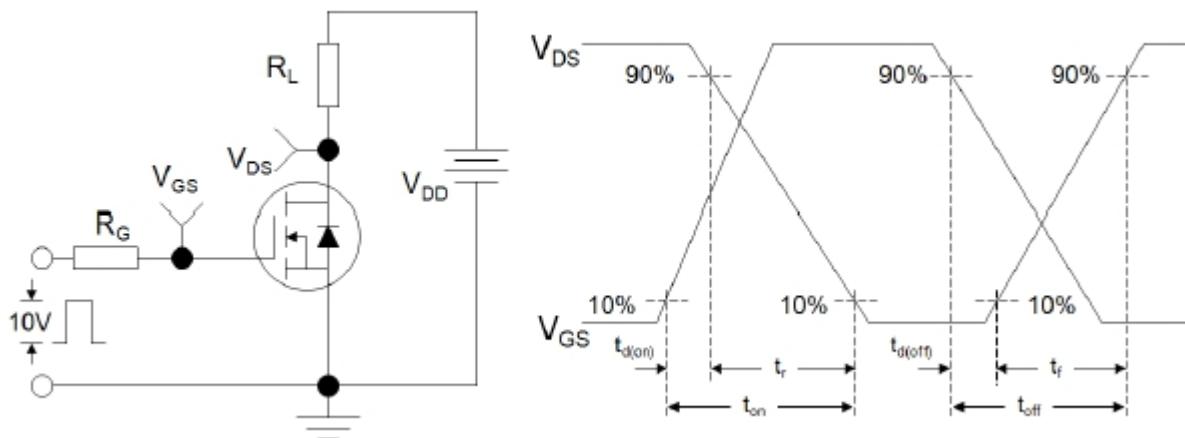


Figure 2: Resistive Switching Test Circuit & Waveforms

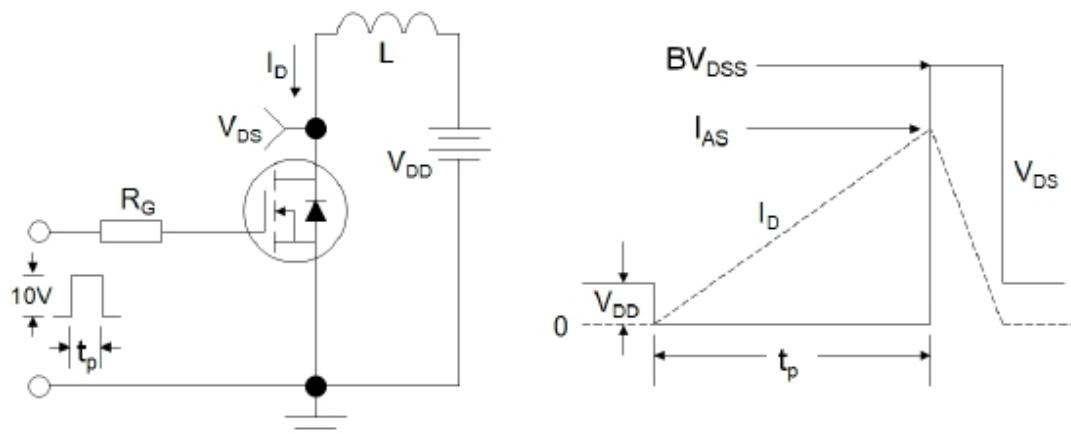
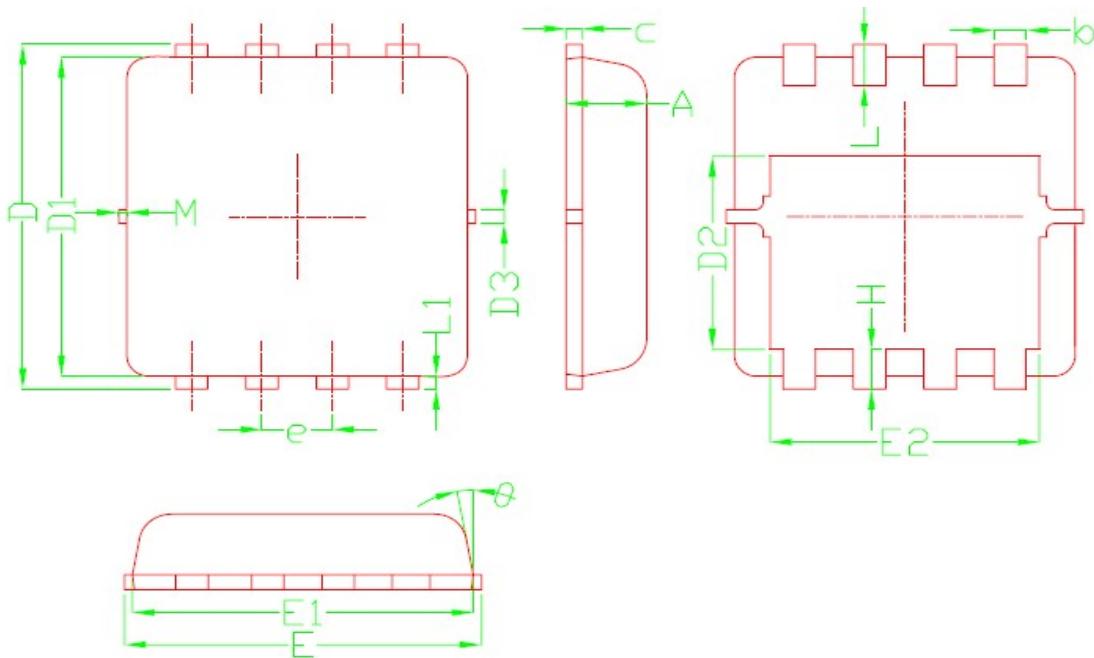


Figure 3: Unclamped Inductive Switching Test Circuit & Waveforms

PDFN3X3-8L Package Information



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.70	0.85	0.027	0.034
b	0.20	0.40	0.007	0.016
c	0.10	0.25	0.004	0.010
D	3.15	3.45	0.124	0.136
D1	2.90	3.20	0.114	0.126
D2	1.54	1.98	0.060	0.080
D3	0.10	0.30	0.004	0.012
E	3.15	3.45	0.124	0.136
E1	3.00	3.25	0.118	0.128
E2	2.29	2.65	0.090	0.104
e	0.65 BSC		0.025 BSC	
H	0.28	0.65	0.011	0.026
Θ	0°	14°	0°	14°
L	0.30	0.50	0.012	0.020
L1	0.13		0.005	
M	---	0.15	---	0.006